

Wait-Only Broadcast Protocols are Easier to Verify

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Abstract

We study networks of processes that all execute the same finite-state protocol and communicate via broadcasts. We are interested in two problems with a parameterized number of processes: the synchronization problem which asks whether there is an execution which puts all processes on a given state; and the repeated coverability problem which asks if there is an infinite execution where a given transition is taken infinitely often. Since both problems are undecidable in the general case, we investigate those problems when the protocol is *Wait-Only*, i.e., it has no state from which a process can both broadcast and receive messages. We establish that the synchronization problem becomes Ackermann-complete, and the repeated coverability problem is in EXPSpace and PSPACE-hard.

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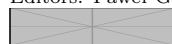


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21 **1** Introduction

22 Distributed systems are at the core of modern computing, and are widely used in critical
 23 applications such as sensor networks, or distributed databases. These systems rely on
 24 protocols to enable communication, maintain coherence and coordination between the
 25 different processes. Because of their distributed nature, such protocols have proved to
 26 be error-prone. As a result, the formal verification of distributed systems has become
 27 an essential area of research. Formal verification of distributed systems presents unique
 28 challenges compared to the one of centralized systems. One of the most significant issue is
 29 the state explosion problem: the behavior of multiple processes that execute concurrently
 30 and exchange information often lead to state spaces that grow exponentially with the number
 31 of processes, making the analysis highly challenging. When the systems are parameterized,
 32 meaning designed to operate for an arbitrary number of processes, which is often the case
 33 in distributed protocols, classical techniques are not useful anymore. The difficulty shifts
 34 from state explosion to dealing with an infinite number of system configurations, which
 35 leads to undecidability in the general case [1]. Despite these challenges, verification becomes
 36 more tractable in certain restricted settings. For instance, in parameterized systems, the
 37 behavior of individual processes needs not always to be explicitly represented, which can
 38 mitigate state explosion. This has motivated researchers to focus on specific subclasses of
 39 systems or communication models where decidability can be achieved without sacrificing too
 40 much expressiveness. One such restriction involves protocols where processes are anonymous
 41 (i.e., without identities), and communicate via simple synchronous mechanisms, such as
 42 rendezvous [9], where two processes exchange a message synchronously. Several variants
 43 of this model have been studied, such as communication via a shared register containing
 44 some value from a finite set [7], or non-blocking rendezvous [10, 4], where a sender is not
 45 prevented from sending a message when no process is ready to receive it. In all these cases,
 46 the processes execute the same protocol, which is described by a finite automaton.

47 A more expressive model is broadcast protocols, introduced by Emerson and Namjoshi [6].
 48 In these protocols, a process can send a broadcast message that is received simultaneously
 49 by all other processes (or by all neighboring processes in a network). Several key verification
 50 problems arise in this context, including the coverability problem, which asks whether a pro-
 51 cess can eventually reach a specific (bad) state starting from an initial configuration. Another
 52 important property is synchronization, which asks whether all processes can simultaneously
 53 reach a given state. Liveness properties, like determining whether infinite executions exist,
 54 ensure that certain behaviors occur indefinitely. While broadcast protocols allow more
 55 powerful communication than rendezvous protocols, this added expressiveness comes at
 56 the cost of increased verification complexity. Indeed, rendezvous communication can be
 57 simulated using broadcasts [8]. However, verification becomes significantly harder: while
 58 coverability and synchronization can be solved in polynomial time for rendezvous proto-
 59 cols [9], they become respectively Ackermann-complete [8, 20] and undecidable (we show it
 60 in this paper) for broadcast protocols. Liveness properties are also undecidable for broadcast
 61 protocols [8]. The challenge in verifying broadcast protocols can be understood through their
 62 relationship with Vector Addition Systems with States (VASS). Rendezvous-based protocols
 63 can be encoded in a VASS, where counters track the number of processes in each state. A
 64 rendezvous is simulated by decreasing the counters corresponding to the sender and receiver
 65 states and increasing the counters for their post-communication states. While using a VASS
 66 for protocols using rendezvous is certainly not the way to go due to the complexity gap
 67 between problems on VASS and the existing known polynomial time algorithms to solve

verification problems, it is interesting to note that this encoding fails for broadcast protocols because a broadcast message must be received by all processes in a given state, requiring a bulk transfer of a counter value—something not expressible in classical VASS without adding powerful operations such as transfer arcs, which leads to an undecidable reachability problem [21]. However, in some cases, verification of broadcast protocols can become easier, complexity-wise. One example is the setting of Reconfigurable Broadcast Networks, in which communication between processes can be lossy [5]. Liveness properties become decidable in that case [5] and even polynomial time [2].

Another such case is given by a syntactic restriction known as *Wait-Only protocols*, introduced in [10] in the context of non-blocking rendezvous communication. In Wait-Only protocols, a process cannot both send and receive a message from the same state. From a practical perspective, Wait-Only protocols were proposed to model the non-blocking rendezvous semantics used in Java's `wait/notify/notifyAll` synchronization mechanism and C's `wait/signal/broadcast` operations with conditional variables, commonly found in multi-threaded programs. In both languages, threads can be awakened by the reception of a message. This naturally leads to distinguishing between action and waiting states: a sleeping thread cannot act on its own and can only be awakened by a signal. This restriction simplifies the possible behaviours of the system, potentially reducing the complexity of the verification. Actually, the coverability problem for Wait-Only broadcast protocols becomes PSPACE-complete [11]. Indeed, when processes are in a state where they can receive broadcasts, they cannot send messages, meaning they will move together to the next state, reducing the need for precise counting. Moreover, when a process is in a broadcasting state, it remains there until it decides to send a message, simplifying execution reconstruction and enabling better verification algorithms. By leveraging these properties, we design algorithms for the synchronization problem and liveness properties, obtaining new decidability results for these challenging problems. Proofs omitted due to space constraints can be found in [12].

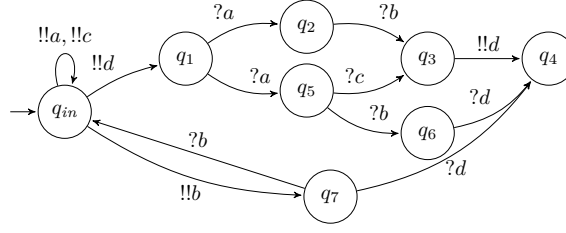
2 Model and Verification Problems

In this section, we provide the description of a model for networks with broadcast communication together with some associated verification problems we are interested in. First we introduce some useful mathematical notations. We use $\mathbb{N}$ to represent the set of natural numbers, $\mathbb{N}_{>0}$ to represent $\mathbb{N} \setminus \{0\}$, and for $n, m \in \mathbb{N}$ with $n \leq m$, we denote by $[n, m]$ the set $\{n, n+1, \dots, m\}$. For a finite set E and a natural $n > 0$, the set E^n represents the n -dimensional vectors with values in E and for $v \in E^n$ and $1 \leq i \leq n$, we use $v(i)$ to represent the i -th value of v . Furthermore, for such a vector v , we denote by $\|v\|$ its dimension n .

Networks of Processes using Broadcast Communication. In the networks we consider, all the processes execute the same protocol, given by a finite state machine. The actions of this machine can either broadcast a message a to the other processes (denoted by $!!a$) or receive a message a (denoted by $?a$). For a finite alphabet of messages Σ , we use the following notations: $!!\Sigma \stackrel{\text{def}}{=} \{!!a \mid a \in \Sigma\}$, $?\Sigma \stackrel{\text{def}}{=} \{?a \mid a \in \Sigma\}$ and $\text{Op}_\Sigma \stackrel{\text{def}}{=} !!\Sigma \cup ?\Sigma$.

► **Definition 2.1.** A protocol P is a tuple (Q, Σ, q_{in}, T) where Q is a finite set of states, Σ is a finite set of messages, $q_{in} \in Q$ is an initial state, and $T \subseteq Q \times \text{Op}_\Sigma \times Q$ is a set of transitions.

For all $q \in Q$, we define $R(q)$ as the set of messages that can be received from state q , given by $\{a \in \Sigma \mid \exists q' \in Q, (q, ?a, q') \in T\}$. A protocol $P = (Q, \Sigma, q_{in}, T)$ is *Wait-Only* whenever for all $q \in Q$, either $\{q' \in Q \mid (q, \alpha, q') \in T \text{ with } \alpha \in ?\Sigma\} = \emptyset$, or $\{q' \in$



■ **Figure 1** Example of a Wait-Only protocol P .

113 $Q \mid (q, \alpha, q') \in T \text{ with } \alpha \in !!\Sigma\} = \emptyset$. In a Wait-Only protocol, a state $q \in Q$ such that
 114 $\{q' \in Q \mid (q, \alpha, q') \in T \text{ with } \alpha \in ?\Sigma\} \neq \emptyset$ is called a *waiting state*. A state that is not a
 115 waiting state is an *action state*. We denote by Q_W the set of waiting states and by Q_A the
 116 set of action states (hence $Q_A = Q \setminus Q_W$). Observe that if $q_{in} \in Q_W$, then no process is ever
 117 able to broadcast messages, for this reason we will always assume that $q_{in} \in Q_A$.

118 For $n \in \mathbb{N}_{>0}$, an n -process *configuration* of a protocol $P = (Q, \Sigma, q_{in}, T)$ is a vector
 119 $C \in Q^n$ where $C(e)$ represents the state of the e -th process in C for $1 \leq e \leq n$. The
 120 configuration is said to be *initial* whenever $C(e) = q_{in}$ for all $1 \leq e \leq n$. The dimension
 121 $\|C\|$ hence characterizes the number of processes in C . For a state $q \in Q$, we use $C^{-1}(q)$ to
 122 represent the set of processes in state q , formally $C^{-1}(q) = \{1 \leq e \leq \|C\| \mid C(e) = q\}$. For a
 123 subset $A \subseteq [1, \|C\|]$ of processes, we use $C(A)$ to identify the set of states of processes in
 124 A , formally $C(A) = \{C(e) \mid e \in A\}$. We let $\mathcal{C}$ [resp. $\mathcal{I}$] be the set of all configurations [resp.
 125 initial configurations] of P .

126 We now define the broadcast network semantics associated to a protocol $P = (Q, \Sigma, q_{in}, T)$.
 127 For configurations $C, C' \in \mathcal{C}$, transition $t = (q, !!a, q') \in T$ and $e \in [1, \|C\|]$, we write
 128 $C \xrightarrow{e, t} C'$ whenever $\|C\| = \|C'\|$, $C(e) = q$, $C'(e) = q'$ and, for all $e' \in [1, \|C\|] \setminus \{e\}$, either
 129 a reception occurs, i.e., $(C(e'), ?a, C'(e')) \in T$, or the process cannot receive a , in which case
 130 $(a \notin R(C(e')) \text{ and } C(e') = C'(e'))$. Intuitively, the e -th process of C broadcasts a message
 131 a , which is received by all processes able to receive it, while the other processes remain
 132 in their current state. We note $C \rightarrow C'$ if there exists $e \in [1, \|C\|]$ and $t \in T$ such that
 133 $C \xrightarrow{e, t} C'$ and use $\rightarrow^*$ [resp. $\rightarrow^+$] for the reflexive and transitive [resp. transitive] closure
 134 of $\rightarrow$. A finite [resp. infinite] execution is a finite [resp. infinite] sequence of configurations
 135 $\rho = C_0 \dots C_\ell$ [resp. $\rho = C_0 \dots$] such that $C_0 \in \mathcal{I}$ and $C_{i-1} \rightarrow C_i$ for all $0 < i \leq \ell$ [resp. for
 136 all $i > 0$]. Its length $|\rho|$ is equal to $\ell + 1$ [resp. ω]. We write Λ [resp. Λ_ω] for the set of finite
 137 [resp. infinite] executions. For an execution $\rho = C_0 C_1 \dots \in \Lambda \cup \Lambda_\omega$ and $0 \leq i < |\rho|$, we use
 138 ρ_i to denote C_i , the i -th configuration. We use $\#\text{proc}(\rho)$ to represent $\|C_0\|$, the number
 139 of processes in the execution. For $0 \leq i < j < |\rho|$, we define $\rho_{i,j} \stackrel{\text{def}}{=} \rho_i \dots \rho_j$. We also let
 140 $\rho_{\geq i} \stackrel{\text{def}}{=} \rho_i \rho_{i+1} \dots$ and $\rho_{\leq i} \stackrel{\text{def}}{=} \rho_0 \dots \rho_i$. For $e \in [1, \#\text{proc}(\rho)]$, we denote by $\rho(e)$ the sequence
 141 of states $\rho_0(e) \rho_1(e) \dots$.

► **Example 2.2.** Figure 1 illustrates an example of a Wait-Only protocol. The waiting states
 are q_1, q_2, q_5, q_6 , and q_7 , with $R(q_5) = \{b, c\}$. Here is one of its execution with 3 processes:

$$\begin{aligned} (q_{in}, q_{in}, q_{in}) &\xrightarrow{1, (q_{in}, !!d, q_1)} (q_1, q_{in}, q_{in}) \xrightarrow{2, (q_{in}, !!d, q_1)} (q_1, q_1, q_{in}) \xrightarrow{3, (q_{in}, !!a, q_{in})} (q_5, q_2, q_{in}) \\ &\xrightarrow{3, (q_{in}, !!c, q_{in})} (q_3, q_2, q_{in}) \xrightarrow{3, (q_{in}, !!b, q_7)} (q_3, q_3, q_7). \end{aligned}$$

142 **Verification Problems.** We investigate two verification problems over broadcast networks:
 143 the synchronization problem (SYNCHRO) and the repeated coverability problem (REPCOVER).

144 SYNCHRO asks, given a protocol $P = (Q, \Sigma, q_{in}, T)$ and a state $q_f \in Q$, whether there exist
 145 $C \in \mathcal{I}$ and $C' \in \mathcal{C}$ such that $C \rightarrow^* C'$, and $C'(e) = q_f$ for all $e \in [1, ||C'||]$. REPCOVER
 146 asks, given a protocol $P = (Q, \Sigma, q_{in}, T)$ and a transition $t = (q, !!a, q') \in T$, whether there
 147 exist $\rho \in \Lambda_\omega$ and $e \in [1, \#proc(\rho)]$, such that for all $i \in \mathbb{N}$, there exists $j > i$ verifying
 148 $\rho_j \xrightarrow{e, t} \rho_{j+1}$.

149 ► **Theorem 2.3.** *SYNCHRO and REPCOVER are undecidable.*

150 We give the proof of the undecidability of SYNCHRO in [12], while the undecidability of
 151 REPCOVER was established in [8, Theorem 5.1]. In the remainder of the paper, we show
 152 that by restricting to Wait-Only protocols, one can regain decidability.

153 ► **Remark 2.4.** Throughout the remainder of this paper, we will consider protocols without
 154 self-loop broadcast transitions of the form $(q, !!a, q)$. Such transitions can be transformed into
 155 two transitions $(q, !!a, p_q)$, $(p_q, !!$, $q)$ where p_q is a new state added to the set of states and $$$
 156 is a new message added to the alphabet. This transformation of the protocol is equivalent to
 157 the original one with respect to SYNCHRO and REPCOVER.$

158 **Vector Addition System with States.** In the following, we will extensively rely on the
 159 model of Vector Addition Systems with States (VASS) which we introduce below. A VASS
 160 is a tuple $\mathcal{V} = (\text{Loc}, \ell_0, \mathbf{X}, \Delta)$ where Loc is a finite set of locations, $\ell_0 \in \text{Loc}$ is the *initial*
 161 location, $\mathbf{X}$ is a finite set of natural variables, called counters, and $\Delta \subseteq \text{Loc} \times \mathbb{Z}^{\mathbf{X}} \times \text{Loc}$ is a
 162 finite set of transitions. A configuration of $\mathcal{V}$ is a pair (ℓ, v) in $\text{Loc} \times \mathbb{N}^{\mathbf{X}}$ where v provides a
 163 value for each counter. The initial configuration is $(\ell_0, \mathbf{0})$ where $\mathbf{0}(x) = 0$ for all $x \in \mathbf{X}$. Given
 164 two configurations (ℓ, v) , (ℓ', v') and a transition $(\ell, \delta, \ell') \in \Delta$, we write $(\ell, v) \xrightarrow{\delta} (\ell', v')$
 165 whenever $v'(x) = v(x) + \delta(x)$ for all counters $x \in \mathbf{X}$. We simply use $(\ell, v) \rightsquigarrow (\ell', v')$ when
 166 there exists $(\ell, \delta, \ell') \in \Delta$ such that $(\ell, v) \xrightarrow{\delta} (\ell', v')$. We denote $\rightsquigarrow^*$ for the transitive and
 167 reflexive closure of $\rightsquigarrow$. An (infinite) execution (or run) of the VASS is a (infinite) sequence
 168 of configurations $(\ell_0, v_0), (\ell_1, v_1), \dots, (\ell_n, v_n)$, starting with the initial configuration and such
 169 that $(\ell_i, v_i) \rightsquigarrow (\ell_{i+1}, v_{i+1})$ for all $0 \leq i < n$.

170 REACH, the well-known reachability problem for VASS, is defined as follows: given a
 171 VASS $\mathcal{V} = (\text{Loc}, \ell_0, \mathbf{X}, \Delta)$ and a location $\ell_f \in \text{Loc}$, is there an execution from $(\ell_0, \mathbf{0})$ to
 172 $(\ell_f, \mathbf{0})$?

173 ► **Theorem 2.5** (Membership [18], Hardness [3, 17]). *REACH is Ackermann-complete.*

174 3 Solving Synchro for Wait-Only Protocols

175 To solve SYNCHRO we show in this section that we can build a VASS that simulates the
 176 behavior of a broadcast network running a Wait-Only protocol. An intuitive way to proceed,
 177 inspired by the counting abstraction proposed for protocols communicating by pairwise
 178 rendez-vous communication [9], consists in having one counter per state of the protocol, that
 179 stores the number of processes that populate that state. Initially, the counter associated with
 180 the initial state can be incremented as much as one wants: this will determine the number of
 181 processes of the execution simulated in the VASS. Then, the simulation of broadcast messages
 182 $(q, !!a, q')$ amounts to decrementing the counter associated to q and increment the counter
 183 associated to q' . However, simulating receptions of the message (e.g. a transition $(p, ?a, p')$),
 184 requires to empty the counter associated to p and transfer its value to the counter associated
 185 to q' . This transfer operation is not something that can be done in VASS unless the model is
 186 extended with special transfer transitions, leading to an undecidable reachability problem [21].

To circumvent this problem, we rely on two properties of Wait-Only protocols: (1) processes that occur to be in the same waiting state follow the same path of reception in the protocol, and end in the same action state (we show how to take care of potential non-determinism in the next section) and (2) processes in an action state cannot be influenced by the behaviour of other processes as they cannot receive any message. Thanks to property (1), instead of precisely tracking the number of processes in each waiting state, we only count the processes in a “waiting region” – a connected component of waiting states populated by processes that will all reach the same action state simultaneously. The waiting region allows us also to monitor when the processes go out from a waiting region to an action state. We will explain later how we use property (2) to handle the transfer of values of counters within the VASS semantics, and thus simulating processes leaving a waiting region.

For the rest of this section, we fix $P = (Q, \Sigma, q_{in}, T)$ a Wait-Only protocol (with Q_W [resp. Q_A] the set of waiting states [resp. action states]) for which we assume w.l.o.g. the existence of an *uncoverable state* $q_u \in Q$ verifying $q \neq q_u$ and $q' \neq q_u$ for all $(q, \alpha, q') \in T$ and $q_u \neq q_{in}$. Furthermore, we consider a final waiting state $q_f \in Q_W$. To ease the reading, we assume in this section that the final state q_f is a waiting state, but our construction could be adapted to the case where q_f is an action state. Moreover, we show in the next section that SYNCHRO in this latter case is easier to solve.

3.1 Preliminary properties

We present here properties satisfied by Wait-Only protocols, which we will rely on for our construction. We first show with Lemma 3.1 that if, during an execution, two processes fulfill two conditions: (i) they are on the same waiting state q_w , and (ii) the next action state they reach (not necessarily at the same time) is the same (namely q_a), then one can build an execution where they both follow the same path between q_w and q_a . This is trivial for *deterministic* protocols (where for all $q \in Q$ and $\alpha \in \text{Op}_\Sigma$, there is at most one transition of the form $(q, \alpha, q') \in T$) and is also true for non-deterministic protocols.

For an execution $\rho \in \Lambda \cup \Lambda_\omega$ of P , an index $0 \leq j < |\rho|$, a waiting state $q \in Q_W$ and a process number $e \in [1, \#\text{proc}(\rho)]$ such that $\rho_j(e) = q$, we define $\mathbf{na-index}(\rho, j, e) = \min\{k \mid j \leq k \leq |\rho| \text{ and } \rho_k(e) \in Q_A\}$, i.e. the first moment after ρ_j where the process e reaches an action state (if such moment does not exist, it is set to $|\rho|$). We note $\mathbf{na-state}(\rho, j, e) \stackrel{\text{def}}{=} \rho_{\mathbf{na-index}(\rho, j, e)}(e)$ the next action state for process e from ρ_j if $\mathbf{na-index}(\rho, j, e) \neq |\rho|$ and otherwise we take the convention that $\mathbf{na-state}(\rho, j, e)$ is equal to q_u , the uncoverable state of the protocol P . Finally, we let $\mathbf{na}(\rho, j, e) = (\mathbf{na-state}(\rho, j, e), \mathbf{na-index}(\rho, j, e))$.

► **Lemma 3.1.** *Let $\rho \in \Lambda \cup \Lambda_\omega$ be an execution of P . If there exist $e_1, e_2 \in [1, \#\text{proc}(\rho)]$ and $0 \leq j < |\rho|$ such that (i) $\rho_j(e_1) = \rho_j(e_2) \in Q_W$, (ii) $\mathbf{na}(\rho, j, e_1) = (q, j_1)$, and (iii) $\mathbf{na}(\rho, j, e_2) = (q, j_2)$ with $j_1 \leq j_2 < |\rho|$, then there exists an execution ρ' of the form $\rho_{\leq j} \rho'_{j+1} \dots \rho'_{j_2} \rho_{\geq j_2+1}$ such that $\rho'_k(e_1) = \rho'_k(e_2) = \rho_k(e_1)$ for all $j+1 \leq k \leq j_1$, and $\rho'_k(e_1) = \rho_k(e_1)$ and $\rho'_k(e_2) = q$ for all $j_1 < k \leq j_2$. In particular $\mathbf{na}(\rho', j, e_1) = \mathbf{na}(\rho', j, e_2) = (q, j_1)$.*

► **Example 3.2.** Consider the protocol P in Figure 1 and the execution ρ of Example 2.2. We have $\rho_2(1) = \rho_2(2) = q_1$ and $\mathbf{na}(\rho, 2, 1) = (q_3, 4)$ and $\mathbf{na}(\rho, 2, 2) = (q_3, 5)$. Applying Lemma 3.1, we build: $\rho' = (q_{in}, q_{in}, q_{in}) \rightarrow (q_1, q_{in}, q_{in}) \rightarrow (q_1, q_1, q_{in}) \rightarrow (q_5, q_5, q_{in}) \rightarrow (q_3, q_3, q_{in}) \rightarrow (q_3, q_3, q_7)$ where process 1 and process 2 follow the same path between q_1 and q_3 . However, consider the following events from (q_1, q_1, q_{in}) : $(q_1, q_1, q_{in}) \rightarrow (q_2, q_5, q_{in}) \rightarrow (q_3, q_6, q_7) \rightarrow (q_4, q_4, q_4)$, here we cannot apply the lemma as from q_1 , processes 1 and 2 reach two different next action states (q_3 and q_4).

The above lemma enables us to focus on a specific subset of executions for SYNCHRO, which we refer to as well-formed. In these executions, at any moment, two processes in the same waiting state and with the same next action state, follow the same path of receptions. Formally, an execution ρ of a protocol P is *well-formed* iff for all $0 \leq i < |\rho|$, for all $e_1, e_2 \in [1, \#\mathbf{proc}(\rho)]$ such that $\rho_i(e_1) = \rho_i(e_2) \in Q_W$ and $\mathbf{na-state}(\rho, i, e_1) = \mathbf{na-state}(\rho, i, e_2) = q$, it holds that $\rho_k(e_1) = \rho_k(e_2)$ for all $i \leq k \leq \mathbf{na-index}(\rho, i, e_1)$. From Lemma 3.1, we immediately get:

► **Corollary 3.3.** *There exists an execution $\rho = C_0 \dots C_n$ such that $C_n(e) = q_f$ for all $e \in [1, \#\mathbf{proc}(\rho)]$ iff there exists a well-formed execution from C_0 to C_n .*

We finally provide a result which will allow us to bound the size of the VASS that we will build from the given Wait-Only protocol. Given an execution $\rho \in \Lambda \cup \Lambda_w$ of P , an index $0 \leq j < |\rho|$ and an action state $q_a \in Q_A$, we define $\mathbf{na-index-set}(\rho, j, q_a)$ as the set of indices where processes in a waiting state at ρ_j will reach q_a , if it is their next action state: $\mathbf{na-index-set}(\rho, j, q_a) \stackrel{\text{def}}{=} \{i \mid \text{there exists } e \in [1, \#\mathbf{proc}(\rho)] \text{ s.t. } \rho_j(e) \in Q_W \text{ and } \mathbf{na}(\rho, j, e) = (q_a, i)\}$.

► **Lemma 3.4.** *For all well-formed executions ρ , for all $0 \leq j < |\rho|$, and for all $q_a \in Q_A$, we have $|\mathbf{na-index-set}(\rho, j, q_a)| \leq |Q_W|$.*

Proof. If we have $|\mathbf{na-index-set}(\rho, j, q_a)| > |Q_W|$, by pigeon hole principle there exist $e_1, e_2 \in [1, \#\mathbf{proc}(\rho)]$ such that $\rho_j(e_1) = \rho_j(e_2) \in Q_W$ and such that $\mathbf{na}(\rho, j, e_1) = (q_a, i_1)$ and $\mathbf{na}(\rho, j, e_2) = (q_a, i_2)$ with $i_1 < i_2$. Consequently $\rho_{i_1}(e_1) = q_a \in Q_A$ and $\rho_{i_1}(e_2) \in Q_W$ hence $\rho_{i_1}(e_1) \neq \rho_{i_1}(e_2)$, which contradicts the definition of well-formedness. ◀

3.2 Building the VASS that Simulates a Broadcast Network

Summaries. We present here the formal tool we use to represent processes in waiting states. We begin by introducing some notations. A *print* $\mathbf{pr}$ is a set of waiting states. Given a non empty subset of states $A = \{q_1, \dots, q_n\} \subseteq Q$, we define a configuration $\dot{A} \in Q^n$ such that $\dot{A}(e) = q_e$ for all $e \in [1, n]$. When $A = \{q\}$, we write $\dot{q} \in Q^1$ instead of $\{\dot{q}\}$. Conversely, given a configuration C , we define $\mathbf{set}(C) = \{q \in Q \mid C^{-1}(q) \neq \emptyset\}$. For two configurations $C \in Q^n$ and $C' \in Q^m$, we let $C \oplus C'$ be the configuration $C'' \in Q^{n+m}$ defined by: $C''(e) = C(e)$ if $1 \leq e \leq n$ and $C''(e) = C'(e - n)$ if $n + 1 \leq e \leq m + n$. A *summary* S is a tuple $(\mathbf{pr}, q_a, k)$ such that $\mathbf{pr} \subseteq Q_W$ is a print, $q_a \in Q_A$ is an *exit state*, and $k \in [1, |Q_W| + 1]$ is an identifier. The label of S is $\mathbf{lab}(S) = (q_a, k)$ and we denote its print by $\mathbf{print}(S)$. Finally, we define a *special* summary **Done**.

In our construction, each process in a waiting state is associated with a summary while processes in action states are handled by the counters of the VASS. Intuitively, a summary $(\mathbf{pr}, q_a, k)$ represents a set of processes lying in the set $\mathbf{pr} \subseteq Q_W$ that will reach the same next action state q_a simultaneously (this restriction is justified by Lemma 3.1). Since the set of waiting states $\mathbf{pr}$ evolve during the simulation, each summary must be uniquely identified. To achieve this, we use an integer $k \in [1, |Q_W| + 1]$. Hence each summary is identified with the pair (q_a, k) . We do not need more than $|Q_W| + 1$ different identifiers, because when a process enters a new summary (i.e it arrives in a waiting state q_w from an action state), aiming for next action state q_a , it either joins an existing summary with exit state q_a , or create a new one. In the latter case, well-formed executions ensure that no existing summary $S = (\mathbf{pr}, q_a, k)$ with exit state q_a is such that $q_w \in \mathbf{pr}$. Otherwise two processes would be in the same state q_w , aiming for the same action state, but at different moments, contradicting Lemma 3.1. Note that we need $|Q_W| + 1$ different identifiers, and not $|Q_W|$ for technical

reasons. Finally, the special summary **Done** is used to indicate when the processes described by a summary reach the exit action state.

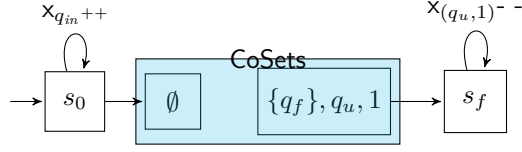
We now describe how summaries evolve with the sequence of transitions. Let $S = (\mathbf{pr}, q_a, k)$ and $S' = (\mathbf{pr}', q_a, k)$ be two summaries (with the same exit state and identifier) and $t = (q, !!b, q') \in T$ such that there exists a configuration C' verifying $\dot{q} \oplus \dot{\mathbf{pr}} \xrightarrow{1,t} \dot{q}' \oplus C'$. We then write $S \xrightarrow{t} S'$ whenever $\mathbf{set}(C') = \mathbf{pr}' \subseteq Q_W$. This represents the evolution of a summary upon reception of message b , when the processes all stay in waiting states, and no new process joins the “waiting region” of the given summary. We write $S \xrightarrow{t,+q'} S'$ whenever $q' \in Q_W$ and $\mathbf{set}(C') \cup \{q'\} = \mathbf{pr}' \subseteq Q_W$. In that latter case, the process responsible for the transition t joins the “waiting region” represented by S . This typically occurs when the process’s next action state is q_a , and it reaches q_a simultaneously with the processes described by S . Finally, we use $S \xrightarrow{t} \mathbf{Done}$ whenever $\mathbf{set}(C') = \{q_a\}$. This represents the evolution (and disappearance) of S when all the processes reach q_a (they all reach it simultaneously).

► **Example 3.5.** Returning to the protocol P of Figure 1, consider the summary $S_0 = (\{q_1\}, q_3, 1)$. Observe that $S_0 \xrightarrow{(q_{in}, !!a, q_{in})} (\{q_2\}, q_3, 1)$ and $S_0 \xrightarrow{(q_{in}, !!a, q_{in})} (\{q_5\}, q_3, 1)$. However, by definition, we do not have $S_0 \xrightarrow{(q_{in}, !!a, q_{in})} (\{q_5, q_2\}, q_3, 1)$. Indeed, processes summarized in S_0 are forced to all go either on q_2 or on q_5 upon receiving a : thanks to Corollary 3.3, we consider only well-formed executions, where all processes summarized in S_0 choose the same next state. Additionally, we have $(\{q_2\}, q_3, 1) \xrightarrow{(q_{in}, !!b, q_7)} \mathbf{Done}$ and $(\{q_1\}, q_4, 1) \xrightarrow{(q_{in}, !!a, q_{in})} (\{q_5\}, q_4, 1) \xrightarrow{(q_{in}, !!b, q_7), +q_7} (\{q_6, q_7\}, q_4, 1) \xrightarrow{(q_{in}, !!d, q_1)} \mathbf{Done}$. However, note that we do not have $(\{q_2\}, q_4, 1) \xrightarrow{(q_{in}, !!b, q_7)} \mathbf{Done}$, since the action state q_3 reached from q_2 upon receiving b is not the exit state q_4 .

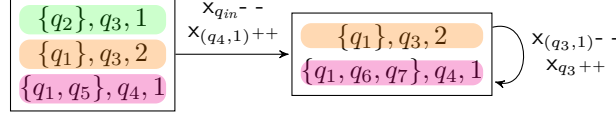
Definition of the VASS. We now explain how we use the summaries in the VASS simulating the executions in the network. First we say that a set $\mathcal{S}$ of summaries is *coherent* if for all distinct pairs $(\mathbf{pr}_1, q_1, k_1), (\mathbf{pr}_2, q_5, k_2) \in \mathcal{S}$ such that $q_1 = q_5$, we have $k_1 \neq k_2$ and $\mathbf{pr}_1 \cap \mathbf{pr}_2 = \emptyset$. We denote by $\mathbf{CoSets}$ the set of coherent sets of summaries. For a set of summaries $\mathcal{S}$ we let $\mathbf{lab}(\mathcal{S}) = \{\mathbf{lab}(S) \mid S \in \mathcal{S}\}$. Observe that, when $\mathcal{S}$ is coherent, for a label $(q, k) \in \mathbf{lab}(\mathcal{S})$, there is a unique $S \in \mathcal{S}$ such that $\mathbf{lab}(S) = (q, k)$.

We define the VASS $\mathcal{V}_P$ simulating the protocol P as follows: $\mathcal{V}_P = (\mathbf{Loc}, s_0, \mathbf{X}, \Delta)$, where $\mathbf{Loc} = \mathbf{CoSets} \cup \{s_0\} \cup \{s_f\}$, the set of counters is $\mathbf{X} = \{x_q \mid q \in Q_A\} \cup \{x_{(q,k)} \mid q \in Q_A, k \in [1, |Q_W| + 1]\}$, and the set of transitions $\Delta = \Delta_0 \cup (\bigcup_{t \in T} \Delta_t) \cup \Delta_e$, is defined as follows:

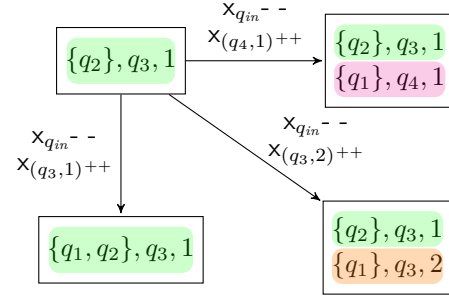
- Δ_0 contains exactly the following transitions:
 - (s_0, δ_0, s_0) where $\delta_0(x_{q_{in}}) = 1$ and $\delta_0(x) = 0$ for all other counters;
 - $(s_0, \mathbf{0}, \emptyset)$ where $\mathbf{0}$ is the null vector (note that the empty set is coherent);
 - $(\{S_f\}, \mathbf{0}, s_f) \in \Delta$ where $S_f = (\{q_f\}, q_u, 1)$;
 - $(s_f, \delta_f, s_f) \in \Delta$ where $\delta_f(x_{(q_u, 1)}) = -1$ and $\delta_f(x) = 0$ for all other counters.
- For $t = (q, !!a, q') \in T$, we have $(\mathcal{S}, \delta, \mathcal{S}') \in \Delta_t$ iff one of the following conditions holds:
 - a. $q' \in Q_A$ and $\mathcal{S} = \{S_1, \dots, S_k\}$. Then, for all $1 \leq i \leq k$, there exists S'_i such that $S_i \xrightarrow{t} S'_i$ and $\mathcal{S}' = \{S'_i \mid 1 \leq i \leq k\} \setminus \{\mathbf{Done}\}$. Moreover, $\delta(x_q) = -1$, $\delta(x_{q'}) = 1$ and $\delta(x) = 0$ for all $x \in \mathbf{X} \setminus \{x_q, x_{q'}\}$. Here we simply update the sets of states populated by processes in waiting states after the transition t . Some summaries may have



■ **Figure 2** The structure of the VASS $\mathcal{V}$.



■ **Figure 3** One successor of location $\{(\{q_2\}, q_3, 1), (\{q_1\}, q_3, 2), (\{q_1, q_5\}, q_4, 1)\}$ (left location) after the broadcast transition $(q_{in}, !!b, q_7)$



■ **Figure 4** Three successors of location $\{(\{q_2\}, q_3, 1)\}$ (top left location) after the broadcast transition $(q_{in}, !!d, q_1)$.

disappeared from $\mathcal{S}$ if the processes represented by this summary have reached their exit action state when receiving a . For now, we only modify the counters associated to the states q and q' . Note that this is well-defined, since we assume in Remark 2.4 that there is no self-loop of the form $(q, !!a, q)$.

b. $q' \in Q_W$, $\mathcal{S} = \{S_1, \dots, S_k\}$, and there exists $1 \leq i \leq k$ and S'_i such that $S_i \xrightarrow{t, +q'} S'_i$.

For all $j \neq i$, there exists S'_j such that $S_j \xrightarrow{t} S'_j$. Then $\mathcal{S}' = \{S'_j \mid 1 \leq j \leq k\} \setminus \{\text{Done}\}$. Moreover, $\delta(x_q) = -1$, $\delta(x_{\text{lab}(S_i)}) = 1$ and $\delta(x) = 0$ for all $x \in X \setminus \{x_q, x_{\text{lab}(S_i)}\}$. In that case, the process having performed the transition joins an existing summary S_i . We have then modified accordingly the counters associated to q and to the appropriate summary.

c. $q' \in Q_W$ and $\mathcal{S} = \{S_1, \dots, S_k\}$. For all $1 \leq j \leq k$, there exists S'_j such that $S_j \xrightarrow{t} S'_j$. Then $\mathcal{S}' = (\{S'_1, \dots, S'_k\} \setminus \{\text{Done}\}) \cup \{(\{q'\}, q_a, k)\}$ for some $q_a \in Q_A$ and $k \in [1, |Q_W| + 1]$ such that $(q_a, k) \notin \text{lab}(\mathcal{S})$. Moreover, $\delta(x_q) = -1$, $\delta(x_{(q_a, k)}) = 1$, and $\delta(x) = 0$ for all $x \in X \setminus \{x_q, x_{(q_a, k)}\}$. This case happens when the process having sent the message a reaches a waiting state, and its expected next action pair (index and state) does not correspond to any existing summary. In that case, it joins a new summary, the counter associated to the state q is decremented and the counter of this new summary is incremented.

■ Finally, Δ_e allows to empty the counters associated to summaries that have disappeared from the set of locations. It is defined as the set of transitions $(\mathcal{S}, \delta, \mathcal{S}')$ such that: $\mathcal{S} = \mathcal{S}'$ and there exists $(q, k) \notin \text{lab}(\mathcal{S})$ with $\delta(x_q) = +1$ and $\delta(x_{(q, k)}) = -1$ and for all other counters, $\delta(x) = 0$.

Transitions from Δ_e allow the transfer of counter values from summaries that have disappeared to the counter of their exit action state. This transfer is not immediate, as it is done through iterative decrements and increments of counters. In particular, it is possible for the execution of the VASS to continue without the counter of a disappeared summary being fully transferred. The processes represented by the counter of a disappeared summary behave in the same way as processes in the exit action state that do not take any action. These counters can be emptied later, but always from a location from where no summary with the same label exists. This ensures that there is no ambiguity between processes in a waiting region and those on an action state that have not yet been transferred to the appropriate counter.

► **Example 3.6.** Figures 2–4 illustrate the VASS $\mathcal{V}_P$ built for the protocol P from Figure 1. Figure 2 shows its overall structure: any run reaching $(s_f, \mathbf{0})$ starts by incrementing the counter of the initial state and ends by decrementing the counter of the summary $(q_f, q_u, 1)$. Here, q_u is an artificial, unreachable state used to ensure that the counted processes must end in q_f (since they cannot be in q_u). Figure 4 illustrates how message receptions and summary creations are handled. The top-left location contains a single summary $(\{q_2\}, q_3, 1)$, representing configurations where all processes on Q_W are in q_2 , all progressing to q_3 . After the broadcast $(q_{in}, !!d, q_1)$, three behaviors may follow. In the first case (right), the sender creates a new summary $(\{q_1\}, q_4, 1)$ (pink). In the second (diagonal), it creates another new summary $(\{q_1\}, q_3, 2)$ (orange), indicating a different arrival time at q_3 . In the third (below), it joins an existing summary, and q_1 is added to the print. Well-formed executions restrict the number of summaries with the same exit state, as there are at most $|Q_W|$ distinct moments where processes can reach a given action state (Lemma 3.4). Figure 3 illustrates summary deletion. The transition $(q_{in}, !!b, q_7)$ causes the sender to join the pink summary (bottom one), incrementing its counter. Processes in the green summary $(\{q_2\}, q_3, 1)$ receive the message via $(q_2, ?b, q_3)$, reaching their exit state q_3 . This summary is deleted in the next location. From there, value of $x_{(q_3, 1)}$ is transferred to the counter x_{q_3} with the loop transition. If it is not taken enough times, $x_{(q_3, 1)}$ may remain non-zero. This does not affect correctness: $x_{(q_3, 1)}$ will be decremented eventually from a state in which there is no summary labeled with $(q_3, 1)$. Not decrementing soon enough only delay the moment the corresponding processes will move from the action state q_3 .

► **Remark 3.7.** In the sequel of this paper, the size of $\mathcal{V}_P$ will be of interest to us. Hence, observe that $|\text{Loc}| = |\text{CoSets}| + 3 \leq 2^{|Q_A| \times (|Q_W| + 1) \times 2^{|Q_W|}} + 3$ (as one summary is composed of a state in Q_A , one label in $[1, |Q_W| + 1]$ and one set of waiting states), and $|X| = |Q_A| + |Q_A| \times (|Q_W| + 1)$.

Soundness of the construction. We show now that if there exists a run in the VASS $\mathcal{V}_P$ from $(s_0, \mathbf{0})$ to $(s_f, \mathbf{0})$, then in the network built from P , there exist $C \in \mathcal{I}$ and $C' \in \mathcal{C}$ such that $C \rightarrow^* C'$ and $C'(e) = q_f$ for all $e \in [1, |C'|]$.

We say that a configuration (ℓ, v) of $\mathcal{V}_P$ is an *S-configuration* if $\ell = \mathcal{S}$ for some $\mathcal{S} \in \text{CoSets}$. The *implementation* of an S-configuration $\lambda = (\mathcal{S}, v)$ is the set of network configurations $\llbracket \lambda \rrbracket \subseteq \mathcal{C}$ defined as follows: $C \in \llbracket \lambda \rrbracket$ if and only if there exists a function $f : [1, |C|] \rightarrow X$ such that $|f^{-1}(x)| = v(x)$ for all $x \in X$, and

- CondImpl1** for all $x_q \in X$ with $q \in Q_A$, we have $C(e) = q$ for all $e \in f^{-1}(x_q)$;
- CondImpl2** for all $x_{(q, k)} \in X$, if there exists $(\text{pr}, q, k) \in \mathcal{S}$ for some $\text{pr} \subseteq Q_W$, then $C(e) \in \text{pr} \cup \{q\}$ for all $e \in f^{-1}(x_{(q, k)})$;
- CondImpl3** for all $x_{(q, k)} \in X$, if $(q, k) \notin \text{lab}(\mathcal{S})$, then $C(e) = q$ for all $e \in f^{-1}(x_{(q, k)})$.

In an implementation of λ , the processes populate states according to the values of the counters. All processes associated with a counter x_q of an action state will populate this exact state (**CondImpl1**). However, processes associated with a counter $x_{(q, k)}$ of a summary do not necessarily populate waiting states. This occurs when the label (q, k) does not appear in $\mathcal{S}$ while the associated counter remains strictly positive. Such a situation arises when a summary has been previously deleted, but its counter has not been emptied. Since all associated processes have already reached the exit action state, the processes associated to the corresponding counter have to populate this active state (**CondImpl3**). If the summary with label (q, k) is active (i.e. its label appears in $\mathcal{S}$), the processes associated with $x_{(q, k)}$ will be in the corresponding waiting region, or in the exit action state (**CondImpl2**). This may seem contradictory, as all processes are supposed to reach their exit state simultaneously.

However, the processes already on the exit action state are “old” processes, that remained after a previous deletion of this summary (cf. **CondImpl3**). These processes will be allowed to send messages only when they are identified with the counter of the active state, meaning when the corresponding transition in Δ_e will be taken. The next lemma allows to build an execution of the protocol P from an execution of the VASS $\mathcal{V}_P$.

► **Lemma 3.8.** *Let λ, λ' be two S -configurations of $\mathcal{V}_P$ and $C \in \llbracket \lambda \rrbracket$. If $\lambda \rightsquigarrow^{\delta} \lambda'$ in $\mathcal{V}_P$ then there exists $C' \in \llbracket \lambda' \rrbracket$ such that $C \rightarrow^* C'$ for P .*

Sketch of Proof. Assume $\lambda = (\mathcal{S}, v)$ and $\lambda' = (\mathcal{S}', v')$. Then either $(\mathcal{S}, \delta, \mathcal{S}') \in \Delta_e$ or $(\mathcal{S}, \delta, \mathcal{S}') \in \Delta_t$ for some $t \in T$. In the first case, we show that if $C \in \llbracket \lambda \rrbracket$, then $C \in \llbracket \lambda' \rrbracket$. Otherwise, let $t = (q, !!a, q')$, there is a process e such that $C(e) = q$. The transition δ in the VASS makes all the summaries and counters evolve according to the reception of the message a . According to the different cases a., b. or c., one can build a configuration $C' \in \llbracket \lambda' \rrbracket$ such that $C \rightarrow C'$. ◀

► **Lemma 3.9.** *If $(s_0, \mathbf{0}) \rightsquigarrow^* (s_f, \mathbf{0})$ in $\mathcal{V}_P$, then there exist $C \in \mathcal{I}$ and $C' \in \mathcal{C}$ such that $C \rightarrow^* C'$ and $C'(e) = q_f$ for all $e \in [1, ||C'||]$.*

Proof. From the definition of $\mathcal{V}_P$, any run from $(s_0, \mathbf{0})$ to $(s_f, \mathbf{0})$ is of the form $(s_0, \mathbf{0}) \rightsquigarrow^* (\emptyset, v_{init}) \rightsquigarrow^* (\{S_f\}, v_f) \rightsquigarrow^* (s_f, \mathbf{0})$ where $v_{init}(x_{q_{in}}) = n$ for some $n \in \mathbb{N}$ and $v_{init}(x) = 0$ for all $x \in X \setminus \{x_{q_{in}}\}$, whereas $v_f(x_{(q_u, 1)}) = v_{init}(x_{q_{in}}) = n$ and $v_f(x) = 0$ for all $x \in X \setminus \{x_{(q_u, 1)}\}$. Define C as the initial configuration in $\mathcal{I}$ with n processes (i.e. $||C|| = n$). Trivially, $C \in \llbracket (\emptyset, v_{init}) \rrbracket$ and with Lemma 3.8 and a simple induction, we get that there exists $C' \in \llbracket (\{S_f\}, v_f) \rrbracket$ such that $C \rightarrow^* C'$. Observe that by definition of v_f , and since $C' \in \llbracket (\{S_f\}, v_f) \rrbracket$, there exists $f : [1, n] \rightarrow X$ such that $f^{-1}(x_{(q_u, 1)}) = [1, n]$. Using **CondImpl2**, we have $C'(e) \in \{q_f, q_u\}$ for all $e \in [1, n]$ with $n = ||C'||$. Since q_u is unreachable by construction, we deduce that $C'(e) = q_f$ for all $e \in [1, ||C'||]$. ◀

Completeness of the construction. Let us first introduce some new definitions. Given a well-formed execution $\rho \in \Lambda \cup \Lambda_\omega$, two indices $0 \leq i < j < |\rho|$ and an action state $q_a \in Q_A$, we define $E_{q_a, j}^{\rho, i}$, the set of processes in waiting states in ρ_i , and whose next action state is q_a , reached at ρ_j . Formally, $E_{q_a, j}^{\rho, i} = \{e \in [1, \#\text{proc}(\rho)] \mid \rho_i(e) \in Q_W \text{ and } \text{na}(\rho, i, e) = (q_a, j)\}$. Furthermore, an S -configuration $\lambda = (\mathcal{S}, v)$ is a *representative* of the configuration ρ_i in ρ iff the following conditions are respected:

CondRepr1 for all $q \in Q_A$, $v(x_q) = |\rho_i^{-1}(q)|$, and

for all $q_a \in Q_A$, there is an injective function $r_{q_a} : \text{na-index-set}(\rho, i, q_a) \rightarrow [1, |Q_W| + 1]$ s.t.:

CondRepr2 $\mathcal{S} = \{(\rho_i(E_{q_a, j}^{\rho, i}), q_a, r_{q_a}(j)) \mid q_a \in Q_A, j \in \text{na-index-set}(\rho, i, q_a)\}$

CondRepr3 for all $q_a \in Q_A$, we have $v(x_{(q_a, r_{q_a}(j))}) = |E_{q_a, j}^{\rho, i}|$ for all $j \in \text{na-index-set}(\rho, i, q_a)$ and $v(x_{(q_a, k)}) = 0$ for all $k \notin r_{q_a}(\text{na-index-set}(\rho, i, q_a))$.

In a representative of the configuration ρ_i , the counters faithfully count the number of processes on active states (**CondRepr1**), and on waiting regions. The set $E_{q_a, j}^{\rho, i}$ gathers exactly the set of processes that populate a same waiting region in a representative of ρ_i in ρ : they all reach the same next action state q_a at the same instant j . The set $\text{na-index-set}(\rho, i, q_a)$ being bounded by $|Q_W|$ (cf. Lemma 3.4), we can associate with each of these indices a unique identifier, given by the injective function r_{q_a} . We use a spare identifier for technical reasons, to handle more easily situations when $|Q_W|$ summaries exist in the location of the VASS, and a new summary is created while one of the previous

summaries is deleted at the next step. Then, **CondRepr2** and **CondRepr3** require that the counters corresponding to the summaries and the summaries themselves reflect faithfully the situation in the configuration ρ_i with respect to ρ . Observe that such a representative is tightly linked to the simulated execution, since we need to know the future behavior of the different processes to determine the different summaries. Finally, if we let $\mathbf{repr}(\rho, i)$ be the set of all S -configurations that are representatives of ρ_i in ρ , we establish the following result before proving the main lemma of this subsection (Lemma 3.11).

► **Lemma 3.10.** *Let ρ be a well-formed execution, and $0 \leq i < |\rho|$. For all $\lambda_i \in \mathbf{repr}(\rho, i)$, there exists $\lambda_{i+1} \in \mathbf{repr}(\rho, i+1)$ such that $\lambda_i \rightsquigarrow^* \lambda_{i+1}$ in $\mathcal{V}_P$.*

Sketch of Proof. From an S -configuration $\lambda_i = (\mathcal{S}_i, v_i)$ in $\mathbf{repr}(\rho, i)$, we can compute the effect on $\mathcal{S}_i$ of the transition $t = (q, !!a, q')$ taken in ρ to go from ρ_i to ρ_{i+1} , and find a matching transition $(\mathcal{S}_i, \delta, \mathcal{S}_{i+1}) \in \Delta_t$ (according to whether q' is a waiting state or not, and in the latter case, whether it joins an existing summary or a new one, depending of the execution ρ). We then obtain a new configuration $\lambda' = (\mathcal{S}_{i+1}, v')$ such that $\lambda_i \rightsquigarrow^{\delta} \lambda'$. Note that λ' is not necessarily in $\mathbf{repr}(\rho, i+1)$: if some summaries have been deleted between λ_i and λ' , the transition $(\mathcal{S}_i, \delta, \mathcal{S}_{i+1})$ in $\mathcal{V}_P$ has not updated the corresponding counters. Hence we need to apply transitions from Δ_e to empty counters corresponding to deleted summaries and accordingly increase corresponding counters on matching action states to obtain $\lambda_{i+1} = (\mathcal{S}_{i+1}, v_{i+1})$ in $\mathbf{repr}(\rho, i+1)$. ◀

► **Lemma 3.11.** *If there exist $C \in \mathcal{I}$, and $C' \in \mathcal{C}$ such that $C \rightarrow^* C'$ in P and $C'(e) = q_f$ for all $e \in [1, ||C'||]$, then $(s_0, \mathbf{0}) \rightsquigarrow^* (s_f, \mathbf{0})$ in $\mathcal{V}_P$.*

Proof. Thanks to Corollary 3.3, we know there exists a well-formed execution ρ from C to C' . Let $K = ||C||$ and $n = |\rho| - 1$. First, consider the sequence $(s_0, \mathbf{0}) \rightsquigarrow^* (s_0, v_0) \rightsquigarrow (\emptyset, v_0)$ where $v_0(x_{q_{in}}) = K$ and for all other counters x , $v_0(x) = 0$. Observe that, since $q_{in} \in Q_A$, $(\emptyset, v_0) \in \mathbf{repr}(\rho, 0)$. By applying inductively Lemma 3.10, we get that $(\emptyset, v_0) \rightsquigarrow^* (\mathcal{S}_1, v_1) \rightsquigarrow^* \dots \rightsquigarrow^* (\mathcal{S}_n, v_n)$ with $(\mathcal{S}_n, v_n) \in \mathbf{repr}(\rho, n)$. By definition, every time that $\mathbf{na-state}(\rho, i, e) = q_u$ for some process e , then $\mathbf{na-index}(\rho, i, e) = n+1$, hence there will always be at most one summary with exit state q_u . So in the execution we build, every time that some summary $(pr, q_u, k) \in \mathcal{S}_i$, we chose $k = 1$. We then have $v_n(x_{q_{u,1}}) = K$ and $v_n(x) = 0$ for all other $x \in X \setminus \{x_{q_{u,1}}\}$. Hence, by construction of $\mathcal{V}_P$, we have $(\mathcal{S}_n, v_n) \rightsquigarrow (s_f, v_n) \rightsquigarrow^* (s_f, \mathbf{0})$. ◀

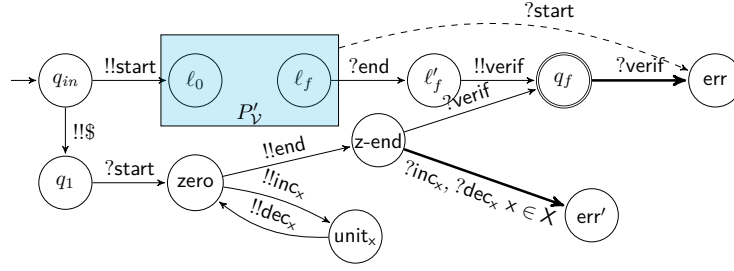
Using Lemma 3.10 and Lemma 3.11 and the fact that the reachability problem for VASS is decidable (see Theorem 2.5) we get the main theorem of this section.

► **Theorem 3.12.** *SYNCHRO restricted to Wait-Only protocols is decidable.*

3.3 Lower Bound for Synchro in Wait-Only Protocols

In this subsection we reduce the reachability problem for VASS to SYNCHRO. We fix a VASS $\mathcal{V} = (\text{Loc}, \ell_0, X, \Delta)$ and a final location $\ell_f \in \text{Loc}$. W.l.o.g., we suppose that any transition in Δ either increments or decrements of 1 exactly one counter. Hence, for a transition $(\ell, \delta, \ell') \in \text{Loc}$, we might describe δ by giving only $\delta(x)$ for the only $x \in X$ such that $\delta(x) \neq 0$.

We explain how to build a protocol $P_{\mathcal{V}}$ that simulates $\mathcal{V}$: it is depicted in Figure 5 in which we don't consider the dashed edge. The states $\mathbf{zero}$ and $\{\mathbf{unit}_x \mid x \in X\}$ represent the evolution of the different counters of the VASS while the blue box $P'_{\mathcal{V}}$ describes the evolution of $\mathcal{V}$ with respect to its locations. Formally, $P'_{\mathcal{V}}$ consists of a set of states $Q_{\mathcal{V}} = \text{Loc} \cup \{\odot\}$



■ **Figure 5** Protocol P associated to a VASS $\mathcal{V}$. The dashed edge $(\ell_f, ?start, err)$ will only be considered in Section 4.2.



■ **Figure 6** Part of P'_V that simulates respectively the transition $(\ell, \delta, \ell') \in \Delta$ with $\delta(x) = 1$ at the left, and $(\ell'', \delta, \ell''') \in \Delta$ with $\delta(x) = -1$ at the right.

and a set of transitions $T_V = \{(\ell, ?inc_x, \ell') \mid (\ell, \delta, \ell') \in \Delta \text{ and } \delta(x) = 1\} \cup \{(\ell, ?dec_x, \ell') \mid (\ell, \delta, \ell') \in \Delta \text{ and } \delta(x) = -1\} \cup \{(\ell, ?m, \odot) \mid m \in \{inc_x, dec_x \mid x \in X\}\}$. Some transitions of T_V are depicted in Figure 6. We then obtain the following theorem.

► **Theorem 3.13.** *SYNCHRO with Wait-Only protocols is Ackermann-complete.*

Sketch of Proof. The upper bound follows from Theorem 3.12 and Theorem 2.5. For the lower bound, we give the ideas that prove that the reduction described above is correct. In particular, there exist $C_0 \in \mathcal{I}$ and $C_f \in \mathcal{C}$ such that $C_0 \rightarrow^* C_f$ in P_V and $C_f(e) = q_f$ for all $e \in [1, ||C_f||]$ if and only if $(\ell_0, \mathbf{0}) \rightsquigarrow^* (\ell_f, \mathbf{0})$ in $\mathcal{V}$.

Assume that $(\ell_0, \mathbf{0}) \rightsquigarrow^* (\ell_f, \mathbf{0})$ in $\mathcal{V}$, and let $K \in \mathbb{N}$ be the maximum value of $\sum_{x \in X} v(x)$ reached during the run. We choose C_0 such that $||C_0|| = K + 1$. The execution proceeds as follows: all processes except one (the leader) broadcast a dummy message $\$$. The leader then broadcasts **start** and reaches ℓ_0 , while the others move to **zero**. These simulate the counter values, and the leader simulates the VASS by moving through the states of P'_V . The value of counter x is represented by the number of processes in $unit_x$. To simulate an increment of x from (ℓ_i, v_i) to (ℓ_{i+1}, v_{i+1}) , a process in **zero** sends inc_x , which the leader receives to transition to ℓ_{i+1} . If the current configuration correctly represents (ℓ_i, v_i) , the new one faithfully represents (ℓ_{i+1}, v_{i+1}) . At the end of the simulation, the leader reaches ℓ_f , and the other processes remain in **zero**. They then broadcast **end**, and the leader (now in ℓ'_f) broadcasts **verif**, gathering all processes in q_f .

To prove the other direction, we must show that the processes cannot cheat in simulating the VASS. First, note that reaching q_f requires exactly one broadcast of **verif**, a second would send the original sender to **err**. This ensures that only one process (the leader) simulates the VASS by moving through P_V . Second, counter updates must follow the VASS transitions: any unauthorized update would cause the leader to receive an unexpected message and transition to the losing state $\odot$. Finally, when the leader reaches ℓ_f , all other processes must be in **zero**. If not, problems occur during the final steps: one process (the helper) broadcasts **end**, which sends the leader to ℓ'_f . Then, all remaining processes must reach **z-end** before the unique **verif** broadcast. If a process in $unit_x$ moves to **z-end**, it must broadcast dec_x , which is

received by other processes in z -end, sending them, including the helper, to err' . As a result, the helper misses $verif$ and cannot reach q_f , preventing a successful execution. Thus, the simulation must end with the leader in ℓ_f and all others in $zero$. $\blacktriangleleft$

4 Synchro is ExpSpace-complete when the Target State is an Action State

Upper bound. We show that SYNCHRO when $q_f \in Q_A$ can be reduced to the mutual reachability problem on VASS, defined as follows: given a VASS $\mathcal{V} = (Loc, \ell_0, X, \Delta)$ and a location ℓ_f , do there exist a run from $(\ell_0, \mathbf{0})$ to $(\ell_f, \mathbf{0})$ and one from $(\ell_f, \mathbf{0})$ to $(\ell_0, \mathbf{0})$. This problem is shown to be in EXPSPACE [16]. More precisely, Leroux establishes the EXPSPACE-membership and provides a bound on the lengths of the runs, for the mutual reachability problem in VAS (Vector Addition Systems). However, by applying the VASS-to-VAS transformation presented in [14], we get the following result.

► **Theorem 4.1** ([16] Corollary 10.6, Transformation of [14]). *Given a VASS $\mathcal{V} = (Loc, \ell_0, X, \Delta)$, if there are two runs $(\ell_0, \mathbf{0}) \rightsquigarrow^* (\ell_f, \mathbf{0})$ and $(\ell_f, \mathbf{0}) \rightsquigarrow^* (\ell_0, \mathbf{0})$, then there are two runs $(\ell_0, \mathbf{0}) \rightsquigarrow^* (\ell_f, \mathbf{0})$ and $(\ell_f, \mathbf{0}) \rightsquigarrow^* (\ell_0, \mathbf{0})$ whose lengths are bounded by $17(|X| + 3)^2 x^{15(|X|+3)^{|X|+5}}$ where $x = (1 + 2(|Loc| + 1)^2)^2$.*

Given a Wait-Only protocol $P = (Q, \Sigma, q_{in}, T)$ and a target state $q_f \in Q_A$, we explain how to transform it into a VASS where mutual reachability is equivalent to SYNCHRO. We build the VASS $\mathcal{V}_P = (Loc, X, s_0, \Delta)$ as described in Section 3.2, with some modifications. The first two modifications simply encode the fact that the target state is not a waiting state anymore, while the third one ensures the mutual reachability.

- We add a state s'_f and the transition $(\{S_f\}, \mathbf{0}, s_f)$ is replaced by two transitions: $(\emptyset, \delta, s'_f)$ and (s'_f, δ', s_f) . Here, $\delta(x_{q_f}) = -1$, $\delta'(x_{q_f}) = 1$, and for all other counters x , $\delta(x) = \delta'(x) = 0$. It prevents the reachability of $(s_f, \mathbf{0})$ to be trivial with the run $(s_0, \mathbf{0}) \rightsquigarrow^* (\emptyset, \mathbf{0}) \rightsquigarrow^* (s_f, \mathbf{0})$.
- The transition (s_f, δ_f, s_f) is replaced by (s_f, δ'_f, s_f) , where $\delta'_f(x_{q_f}) = -1$ and $\delta'_f(x) = 0$ for all other counters x .
- We add a transition $(s_f, \mathbf{0}, s_0)$ to allow the resetting of $\mathcal{V}_P$ to s_0 after reaching s_f .

► **Lemma 4.2.** *There exists $C \in \mathcal{I}$ and $C' \in \mathcal{C}$ such that $C \rightarrow^* C'$ and, for all $e \in [1, ||C'||]$, $C'(e) = q_f$ if and only if there are two runs $(s_0, \mathbf{0}) \rightsquigarrow^* (s_f, \mathbf{0})$ and $(s_f, \mathbf{0}) \rightsquigarrow^* (s_0, \mathbf{0})$ in $\mathcal{V}_P$.*

Sketch of Proof. Assume that there exist $C \in \mathcal{I}$ and $C' \in \mathcal{C}$ such that $C \rightarrow^* C'$ and, for all $e \in [1, ||C'||]$, $C'(e) = q_f$. Since the main body of $\mathcal{V}_P$ is the same as in Section 3.2 we can, like in Lemma 3.11, build a run of $\mathcal{V}_P$ from $(s_0, \mathbf{0})$ to $(s_f, \mathbf{0})$. By construction of $\mathcal{V}_P$, $(s_f, \mathbf{0}) \rightsquigarrow^* (s_0, \mathbf{0})$. Assume now that $(s_0, \mathbf{0}) \rightsquigarrow^* (s_f, \mathbf{0})$ (and $(s_f, \mathbf{0}) \rightsquigarrow^* (s_0, \mathbf{0})$). Observe that it might be the case that the run looks like $(s_0, \mathbf{0}) \rightsquigarrow^* (s_f, v_1) \rightsquigarrow^* (s_0, v_1) \rightsquigarrow^* (s_f, v_2) \dots (s_0, v_k) \rightsquigarrow^* (s_f, \mathbf{0})$. By construction of the VASS, we know that there is an execution $(s_0, \mathbf{0}) \rightsquigarrow^* (\emptyset, v'_0) \rightsquigarrow^* (\emptyset, v''_0) \rightsquigarrow^* (s'_f, v'''_0) \rightsquigarrow^* (s_f, v''_0) \rightsquigarrow^* (s_f, v_1)$. Here v'_0 is the valuation obtained after having increased the counter $x_{q_{in}}$, v''_0 is the valuation obtained just before going to s'_f , and v_1 is obtained after having decremented counter x_{q_f} . Adapting the proof of Lemma 3.9 to this case, we deduce the existence of an execution of the protocol from an initial configuration C_0 to a configuration C'_0 such that $|C'^{-1}_0(q)| = 0$ for all $q \in Q_W$, and $|C'^{-1}_0(q)| = v''_0(x_q) + \sum_{i=1, \dots, |Q_W|+1} v''_0(x_{(q,i)})$ for all $q \in Q_A$. From the portion of run of the VASS $(s_0, v_1) \rightsquigarrow^* (\emptyset, v'_1) \rightsquigarrow^* (\emptyset, v''_1) \rightsquigarrow^* (s'_f, v'''_1) \rightsquigarrow^* (s_f, v''_1) \rightsquigarrow^* (s_f, v_2)$, we

similarly get another sequence of configurations of the protocol from a configuration C_1 (not necessarily initial because v_1 might not be equal to 0 for some counters other than $x_{q_{in}}$) to a configuration C'_1 such that $C_1 \rightarrow^* C'_1$ and $|C'^{-1}_1(q)| = 0$ for all $q \in Q_W$. Observe however that these two sequences can be merged into a single one, of size $v'_0(x_{q_{in}}) + (v'_1(x_{q_{in}}) - v_1(x_{q_{in}}))$ (the second part corresponds to processes added in q_{in} with transition (s_0, δ_{in}, s_0) , and $\delta_{in}(x_{q_{in}}) = 1$). The execution then first behaves like the execution from C_0 to C'_0 , potentially leaving some processes in the initial state (in particular processes from $v'_1(x_{q_{in}}) - v_1(x_{q_{in}})$). Once this first part is over, all the processes are either in the initial state, or in an action state (because $C'^{-1}_0(q) = 0$ for all $q \in Q_W$). Then, one can simulate the second sequence, (processes already in q_f from the first execution won't be affected because they are in an action state, so they won't receive any message. This would not be true if $q_f \in Q_W$). Since the execution of $\mathcal{V}_P$ eventually reaches $(s_f, \mathbf{0})$, it means that it reaches (s_f, v_{k+1}) where $v_{k+1}(x) = 0$ for all $x \neq x_{q_f}$. Then, by iterating the construction described above, one obtains an execution of P from an initial configuration to a configuration C_f such that $C_f(e) = q_f$ for all $e \in [1, ||C_f||]$. ◀

As runs of doubly exponential length can be guessed in exponential space in the size of the VASS and $\text{EXPSPACE} = \text{NEXPSPACE}$, we get the following theorem using Remark 3.7 and Theorem 4.1. Observe that even if the number of locations is doubly exponential in the size of the protocol, the bound of Theorem 4.1 is polynomial in $|\text{Loc}|$ and doubly exponential in $|X|$, hence lengths of the runs to guess remain doubly exponential in the size of the protocol.

► **Theorem 4.3.** *SYNCHRO for Wait-Only protocols is in EXPSPACE when $q_f \in Q_A$.*

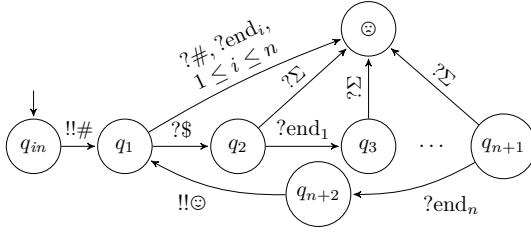
Lower bound. EXPSPACE-hardness follows from a reduction of the coverability problem in VASS, which is EXPSPACE-hard [19], and stated as follows: given a VASS $\mathcal{V} = (\text{Loc}, \ell_0, X, \Delta)$ of dimension $d \in \mathbb{N}$, and a location $\ell_f \in \text{Loc}$, decide whether there is an execution from $(\ell_0, \mathbf{0})$ to (ℓ_f, v) for some $v \in \mathbb{N}^d$.

The reduction uses the protocol depicted in Figure 5, this time including the dashed edge but excluding the thick edges: q_f is now an action state. Then ℓ_f is coverable iff there exists an execution $C_0 \rightarrow^* C_f$ in which all processes reach q_f . The execution of the VASS can be simulated in the protocol like in Section 3.3. The processes that may still remain in the states unit_x can reach z-end even when ℓ'_f is populated (recall that all the thick edges have been removed). Once this is done, all processes can gather in q_f . Conversely, if there exists an execution in which all processes reach q_f , it means that ℓ_f is coverable. Let e_0 be the first process to broadcast **start**, then an execution of the VASS covering ℓ_f can be built based on the sequence of states visited by e_0 between ℓ_0 and ℓ_f . This relies on three keys observations: (1) The process e_0 must visit ℓ_f , otherwise it cannot reach q_f . (2) When e_0 is between ℓ_0 and ℓ_f , e_0 is the only process in this region. If another process attempts to join by broadcasting **start**, e_0 will receive the message and go to **err**, preventing it from reaching q_f . (3) When e_0 reaches ℓ_0 for the first time, no other process is in the states $\{\text{unit}_x \mid x \in X\}$, since **start** is sent for the first time at this point. Hence, with Theorem 4.3, it establishes the following result.

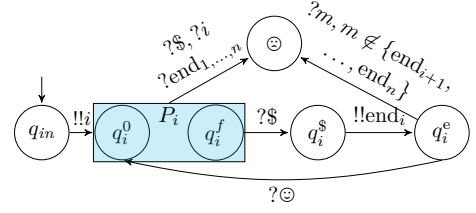
► **Theorem 4.4.** *SYNCHRO for Wait-Only protocols is EXPSPACE-complete when $q_f \in Q_A$.*

5 Solving the Repeated Coverability problem for Wait-Only Protocols

Upper Bound. We show that REPCOVER on Wait-Only protocols is in EXPSPACE, via the repeated coverability problem in VASS: given a VASS $\mathcal{V} = (\text{Loc}, \ell_{init}, X, \Delta)$ and a location



■ **Figure 7** The part of P with transition $t_f = (q_{n+2}, !!@, q_1)$. We write $?\Sigma$ for $?m, \forall m \in \Sigma$.



■ **Figure 8** The part of P simulating automaton i .

596 $\ell_f \in \text{Loc}$, is there an infinite execution $(\ell_{init}, \mathbf{0}) \rightsquigarrow (\ell_1, v_1) \rightsquigarrow \dots$ such that, for all $i \in \mathbb{N}$,
 597 there exists $j > i$ such that $\ell_j = \ell_f$? We rely on the following theorem.

598 ► **Theorem 5.1** (Theorem 3.1 of [13]). *For a VASS $\mathcal{V} = (\text{Loc}, \ell_{init}, \mathbf{X}, \Delta)$, the repeated
 599 coverability problem is solvable in $O(\log(l) + \log(|\text{Loc}|))2^{c|\mathbf{X}| \log(|\mathbf{X}|)}$ nondeterministic space for
 600 some constant c independent of $\mathcal{V}$, and where the absolute values of components of vectors in
 601 Δ are smaller than l .*

602 Let $P = (Q, \Sigma, q_{in}, T)$ be a Wait-Only protocol and $t_f = (q, !!a, q')$ the transition that
 603 has to occur infinitely often. We build a VASS $\mathcal{V}_P$ based on the construction presented
 604 in Section 3.2. This time, we add a new set of states $\text{CoSets}_@ = \{(\mathcal{S}, @) \mid \mathcal{S} \in \text{CoSets}\}$.
 605 and the set of transitions $\{(\mathcal{S}, \delta, (\mathcal{S}', @)) \mid (\mathcal{S}, \delta, \mathcal{S}') \in \Delta_{t_f}\} \cup \{(\mathcal{S}, @, \mathbf{0}, \mathcal{S}) \mid \mathcal{S} \in \text{CoSets}\}$.
 606 Hence, when a process takes the transition t_f , it is highlighted in $\mathcal{V}_P$ by going in a location
 607 tagged with $@$, before continuing the execution. Taking the protocol of Figure 1, with $t_f =$
 608 $(q_{in}, !!d, q_1)$, the previous transition from the location of the VASS $\{(\{q_3\}, q_4, 1)\}$ to the loca-
 609 tion $\{(\{q_3\}, q_4, 1), (\{q_1\}, q_6, 1)\}$ (see Figure 4) is now transformed into two transitions in the
 610 VASS we build here: one from $\{(\{q_3\}, q_4, 1)\}$ to the location $\{(\{q_3\}, q_4, 1), (\{q_1\}, q_6, 1)\}, @$
 611 and one from $\{(\{q_3\}, q_4, 1), (\{q_1\}, q_6, 1)\}, @$ to $\{(\{q_3\}, q_4, 1), (\{q_1\}, q_6, 1)\}$. There is an
 612 execution of P with a process that takes t_f infinitely often iff there is an execution
 613 of $\mathcal{V}_P$ where one state in $\text{CoSets}_@$ is visited infinitely often. The procedure to decide
 614 REPCOVER is then: guess a location ℓ_f in $\text{CoSets}_@$ and solve the repeated coverabil-
 615 ity problem for $\mathcal{V}_P$ and ℓ_f . From Remark 3.7 and Theorem 5.1, this can be done in
 616 $O(\log(2) + (|Q| + 1)^2 \times 2^{|Q|} + 2)2^{c(|Q|+1)^3 \log((|Q|+1)^3)}$ nondeterministic space. The overall
 617 procedure is then in $\text{NEXPSpace} = \text{EXPSpace}$. Hence, the following theorem.

618 ► **Theorem 5.2.** *REPCOVER is in EXPSpace.*

619 ► **Remark 5.3.** One could define REPCOVER with a reception transition that has to occur
 620 infinitely often. The VASS we described hereabove can be adapted by enlarging each location
 621 with the current state of the witness process. This can also be done in EXPSpace.

622 **Lower bound.** We reduce the intersection non-emptiness problem for deterministic finite
 623 automata, which is known to be PSPACE-complete [15], to REPCOVER. Let $\mathcal{A}_1, \dots, \mathcal{A}_n$ be
 624 deterministic, complete finite automata, with $\mathcal{A}_i = (A, Q_i, q_i^0, q_i^f, \Delta_i)$ for $1 \leq i \leq n$. The
 625 reduction assumes a single accepting state per automaton, which does not affect complexity.
 626 We denote by A^* the set of words over the alphabet A , and extend each transition function
 627 Δ_i to A^* by defining: $\Delta_i^*(q, \varepsilon) = q$, and $\Delta_i^*(q, wa) = \Delta_i(\Delta_i^*(q, w), a)$ for all $w \in A^*$ and
 628 $a \in A$.

629 We define a protocol $P = (Q, \Sigma, q_{in}, T)$ composed of several components. For each
 630 automaton $\mathcal{A}_i$, P includes a component $P_i = (Q^i, T^i)$ (see Figure 8), where $Q^i = Q_i$ and

631 $T^i = \{(q_1^i, ?a, q_2^i) \mid (q_1^i, a, q_2^i) \in \Delta_i\}$. The main control component (see Figure 7) includes
 632 the transition $t_f = (q_{n+2}, !!\odot, q_1)$, which must occur infinitely often. A process taking t_f
 633 infinitely often also receives infinitely many sequences of messages $\$ \cdot \text{end}_1 \cdot \text{end}_2 \dots \text{end}_n$,
 634 where each end_i is broadcast by the component simulating $\mathcal{A}_i$. In addition to transitions
 635 in Figures 7 and 8, T also includes: $\{(q_{in}, !!a, q_{in}) \mid a \in A\} \cup \{(q_{in}, !!\$, q_{in})\}$. One can show
 636 that there exists a word $w = a_1 \dots a_n$ in the intersection of the n automata iff there is an
 637 execution in which t_f is taken infinitely often.

638 ► **Theorem 5.4.** *REPCOVER is PSPACE-hard.*

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A

 Synchro is undecidable

In order to prove undecidability of SYNCHRO, we reduce the halting problem of a 2-counter machine.

2-counter machines

A 2-counter machine is a tuple $M = (S, s_{in}, s_f, x_1, x_2, \Delta)$ such that S is a finite set of states, $s_{in} \in S$ is the initial state, $s_f \in S$ is the final state, x_1 and x_2 are two counters that take their values in $\mathbb{N}$, and $\Delta \subseteq S \times \text{Op} \times S$ is a finite set of transitions, with $\text{Op} = \{x++, x--, x=0 \mid x \in \{x_1, x_2\}\}$. A configuration of the machine is a tuple (s, x_1, x_2) with $s \in S$, and $x_1 \in \mathbb{N}$ (resp. $x_2 \in \mathbb{N}$) is the value of counter x_1 (resp. x_2).

Consider a transition $\delta = (s, \text{op}, s') \in \Delta$, then for $x_1, x_2 \in \mathbb{N}$, we have:

- $(s, x_1, x_2) \xrightarrow{\delta} (s', x_1 + 1, x_2)$ iff $\text{op} = x_1++$;
- $(s, x_1, x_2) \xrightarrow{\delta} (s', x_1, x_2 + 1)$ iff $\text{op} = x_2++$;
- $(s, x_1, x_2) \xrightarrow{\delta} (s', x_1 - 1, x_2)$ iff $x_1 > 0$ and $\text{op} = x_1--$;
- $(s, x_1, x_2) \xrightarrow{\delta} (s', x_1, x_2 - 1)$ iff $x_2 > 0$ and $\text{op} = x_2--$;
- $(s, x_1, x_2) \xrightarrow{\delta} (s', x_1, x_2)$ iff $x_1 = 0$ and $\text{op} = (x_1=0)$;
- $(s, x_1, x_2) \xrightarrow{\delta} (s', x_1, x_2)$ iff $x_2 = 0$ and $\text{op} = (x_2=0)$;

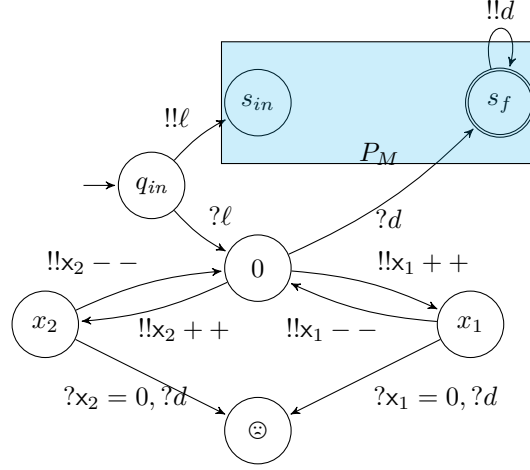
We let $\Delta_{\text{test}} = \{(s, \text{op}, s') \in \Delta \mid \text{op} \in \{x_1=0, x_2=0\}\} \subseteq \Delta$. Given two configurations (s, x_1, x_2) and (s', x'_1, x'_2) , we write $(s, x_1, x_2) \rightsquigarrow (s', x'_1, x'_2)$ whenever there exists a transition $\delta \in \Delta$ such that $(s, x_1, x_2) \xrightarrow{\delta} (s', x'_1, x'_2)$. Finally we denote $\rightsquigarrow^*$ the reflexive and transitive closure of $\rightsquigarrow$. We assume that Δ is deterministic, and that s_f has no outgoing transition. The halting problem asks whether $(s_{in}, 0, 0) \rightsquigarrow^* (s_f, 0, 0)$ and it is undecidable.

Simulation.

Given a machine $M = (S, s_{in}, s_f, x_1, x_2, \Delta)$, we define a protocol P depicted in Figure 9, where P_M is defined as follows: $P_M = (S \cup \{\odot\}, T_M)$ with $T_M = \{(s, ?\text{op}, s') \mid (s, \text{op}, s') \in \Delta \setminus \Delta_{\text{test}}\} \cup \{(s, !!x=0, s') \mid (s, x=0, s') \in \Delta_{\text{test}}\} \cup \{(s, ?\text{op}, \odot) \mid s \in S, \text{op} \in \{x++, x-- \mid x \in \{x_1, x_2\}\}\}$.

If $(s_{in}, 0, 0) \rightsquigarrow^* (s_f, 0, 0)$, there is an execution of the protocol that gathers all the processes in s_f . The number of processes needed is $1 + \max(x_1 + x_2)$ where $\max(x_1 + x_2)$ denotes the maximum value of the sum of the counters during the execution of M . Initially, a single process (the leader), broadcasts ℓ , and goes to s_{in} , while all the others go to the state 0. From that point onward, the configurations of the 2-counter machines are encoded in the protocol's configurations as follows: the state of the leader process determines the location of M , and the number of processes in state x_1 (resp. x_2) represents the value of counter x_1 (resp. x_2).

From a location s , if the transition taken in the execution of M belongs to Δ_{test} , the leader process broadcasts the corresponding message $!!x_i = 0$. Since the counter x_i is zero at that moment, no process is in state x_i , allowing the simulation to continue. If the transition involves an increment (respectively, a decrement) of counter x_i , then, by our choice of the number of processes, there are some processes in state 0 (respectively, in state x_i). One such process broadcasts the message $!!x_i++$ (respectively, $!!x_i--$), which causes the leader to go to the next location. The protocol configuration remains equivalent to the configuration of M . Since $(s_f, 0, 0)$ is reached in M , this simulation ensures that the leader process eventually reaches state s_f , while all other processes remain in state 0. At that moment, the leader broadcasts $!!d$, bringing all processes to s_f .



■ **Figure 9** Protocol P associated to a machine $M = (S, s_{in}, s_f, x_1, x_2, \Delta)$.

Conversely, if there exists an execution of the protocol that gathers all processes in s_f , then at some point, one process must have broadcast $!!\ell$, after which all other processes must have reached state 0. From this point, if any process deviates from the intended behavior—such as a counter sending an incorrect operation to the leader or the leader sending a zero-test message when the corresponding counter is not empty—an erroneous message is received by some processes, leading them to the sink state $\ominus$, from which it is impossible to gather all processes in s_f . When the leader process eventually reaches s_f (which must happen since the execution gathers all processes in s_f), the only way to ensure that all other processes join is by broadcasting $!!d$. If any process is not in state 0 at that moment, it will be sent to the sink state $\ominus$. Consequently, the corresponding execution in the 2-counter machine must be correct and must reach $(s_f, 0, 0)$.

B Proof of Lemma 3.1

Proof. Let $\rho \in \Lambda \cup \Lambda_\omega$ be an execution of P such that there exist $e_1, e_2 \in [1, \#\text{proc}(\rho)]$ and $0 \leq j_0 < |\rho|$ with $\rho_{j_0}(e_1) = \rho_{j_0}(e_2) \in Q_W$ and $\mathbf{na}(\rho, j_0, e_1) = (q, j_1)$ and $\mathbf{na}(\rho, j_0, e_2) = (q, j_2)$ with $j_1 \leq j_2 < |\rho|$.

We define the sequence of configurations $\rho' = \rho'_{j_0+1} \cdots \rho'_{j_2}$ as follows:

- for all $j_0 < k \leq j_1$, $\rho'_k(e_2) = \rho_k(e_1)$ and for all $e \neq e_2$, $\rho'_k(e) = \rho_k(e)$;
- for all $j_1 < k \leq j_2$, $\rho'_k(e_2) = q$ and for all $e \neq e_2$, $\rho'_k(e) = \rho_k(e)$.

Observe that $\rho_{j_2}(e_2) = q$ as $\mathbf{na}(\rho, j_0, e_2) = (q, j_2)$. Hence, $\rho'_{j_2} = \rho_{j_2}$. Hence, it remains to prove that $\rho_{j_0} \rightarrow \rho'_{j_0+1} \rightarrow \cdots \rightarrow \rho'_{j_2}$ in order to prove that $\rho_{\leq j_0} \cdot \rho' \cdot \rho_{\geq j_2+1}$ is an execution.

We do so by induction: we let $\rho'_{j_0} = \rho_{j_0}$ and we prove by induction on $j_0 \leq k \leq j_2$ that $\rho'_{j_0} \rightarrow \rho'_{j_0+1} \rightarrow \cdots \rightarrow \rho'_k$.

For $k = j_0$, there is nothing to do. Let $j_0 \leq k < j_2$ and assume that $\rho'_{j_0} \rightarrow \rho'_{j_0+1} \rightarrow \cdots \rightarrow \rho'_k$. Let us prove that $\rho'_k \rightarrow \rho'_{k+1}$. To do so, let $\rho_k \xrightarrow{e_0, t} \rho_{k+1}$ with $t = (q, !!a, q')$. We distinguish between three cases:

- Case $k < j_1$. First observe that $k < j_1 = \mathbf{na-index}(\rho, j_0, e_1) \leq \mathbf{na-index}(\rho, j_0, e_2) = j_2$, hence $\rho_k(e_1) \in Q_W$, $\rho_k(e_2) \in Q_W$ and so $e_0 \neq e_1$ and $e_0 \neq e_2$.

775 Observe that we are in the case where $\rho'_k(e_2) = \rho_k(e_1)$ and $\rho'_{k+1}(e_2) = \rho_{k+1}(e_1)$. As
 776 $\rho_k \rightarrow \rho_{k+1}$, either $(\rho'_k(e_2), ?a, \rho'_{k+1}(e_2)) = (\rho_k(e_1), ?a, \rho_{k+1}(e_1)) \in T$ or $a \notin R(\rho_k(e_1)) =$
 777 $R(\rho'_k(e_2))$ and $\rho'_{k+1}(e_2) = \rho_{k+1}(e_1) = \rho_k(e_1) = \rho'_k(e_2)$. Hence, as for all other process e ,
 778 $\rho'_k(e) = \rho_k(e)$ and $\rho'_{k+1}(e) = \rho_{k+1}(e)$, it holds that $\rho'_k \xrightarrow{e_0, t} \rho'_{k+1}$.
 779 ■ Case $k > j_1$. First observe that $k < j_2 = \mathbf{na-index}(\rho, j_0, e_2)$, hence $\rho_k(e_2) \in Q_W$, and
 780 so $e_0 \neq e_2$.
 781 Observe that we are in the case where $\rho'_k(e_2) = \rho'_{k+1}(e_2) = q$. Recall that $q \in Q_A$. Hence,
 782 $a \notin R(q) = \emptyset$.
 783 As $\rho'_k(e) = \rho_k(e)$ and $\rho'_{k+1}(e) = \rho_{k+1}(e)$ for all other process e , it holds that $\rho'_k \xrightarrow{e, t} \rho'_{k+1}$.
 784 As in the previous case, we conclude that $\rho'_k \xrightarrow{e_0, t} \rho'_{k+1}$.
 785 ■ Case $k = j_1$. In that case $\rho'_{j_1}(e_2) = \rho_{j_1}(e_1) = q$ and $\rho'_{j_1+1}(e_2) = q$. As $q \in Q_A$,
 786 $a \notin R(q) = \emptyset$ and so, as $\rho'_k(e) = \rho_k(e)$ and $\rho'_{k+1}(e) = \rho_{k+1}(e)$ for all other process $e \neq e_2$,
 787 it holds that $\rho'_k \xrightarrow{e_0, t} \rho'_{k+1}$.

788

789 C Proofs of Section 3.2

790 Let $\lambda = (\mathcal{S}, v)$ and $\lambda' = (\mathcal{S}', v')$ be two S-configurations and $t = (q, !!a, q') \in T$. When
 791 $\lambda \xrightarrow{\delta} \lambda'$ and $(\mathcal{S}, \delta, \mathcal{S}') \in \Delta_t$, we say that $\lambda \xrightarrow{t} \lambda'$.

792 **Proof of Lemma 3.8.** Let $\lambda \xrightarrow{\delta} \lambda'$ with $\lambda = (\mathcal{S}, v)$ and $\lambda' = (\mathcal{S}', v')$. Let $C \in \llbracket \lambda \rrbracket$ with
 793 $\|C\| = n$ and $f : [1, n] \rightarrow \mathbf{X}$.

- 794 ■ If $\delta \in \Delta_e$, then $\mathcal{S} = \mathcal{S}'$ and let $(p, k) \notin \mathbf{lab}(\mathcal{S})$ such that $\delta(x_p) = 1$ and $\delta(x_{p,k}) = -1$. In
 795 that case, we let $C' = C$ and $C \rightarrow^* C'$. It remains to show that $C = C' \in \llbracket \lambda' \rrbracket$. For that,
 796 we will build another function $f' : [1, n] \rightarrow \mathbf{X}$, that reflects the modification of counters $x_{p,k}$
 797 and x_p . Since $\delta(x_{p,k}) = -1$, $v(x_{p,k}) > 0$ and hence $|f^{-1}(x_{p,k})| > 0$. Thus, let $e \in f^{-1}(x_{p,k})$.
 798 Since $(p, k) \notin \mathbf{lab}(\mathcal{S})$, by **CondImpl3**, $C(e) = C'(e) = p$. Then, we let $f'(e) = x_p$, and
 799 for all other processes $e' \in [1, n]$, we let $f'(e) = f(e)$. Then, $|f'^{-1}(x_p)| = |f^{-1}(x_p)| + 1$,
 800 $|f'^{-1}(x_{p,k})| = |f^{-1}(x_{p,k})| - 1$, and for all other counter x , $|f'^{-1}(x)| = |f^{-1}(x)|$. Then,
 801 $v'(x_p) = v(x_p) + 1 = |f^{-1}(x_p)| + 1 = |f'^{-1}(x_p)|$, $v'(x_{p,k}) = v(x_{p,k}) - 1 = |f^{-1}(x_{p,k})| - 1 =$
 802 $|f'^{-1}(x_{p,k})|$, and for all other counter $v'(x) = v(x) = |f'^{-1}(x)|$. Since $C'(e) = p$, it is easy
 803 to see that f' meets the requirements **CondImpl1**, **CondImpl2** and **CondImpl3**, hence
 804 $C' \in \llbracket \lambda' \rrbracket$.
- 805 ■ Otherwise, $\delta \notin \Delta_e$ and let $t = (q, !!a, q')$ such that $\lambda \xrightarrow{t} \lambda'$. Since $\delta(x_q) = -1$, $v(x_q) > 0$
 806 and hence there exists $e_q \in [1, n]$ such that $f(e_q) = x_q$, and, by **CondImpl1**, $C(e_q) = q$.
 807 We build C' as follows: $C'(e_q) = q'$. Then for all other $e \in [1, n]$, if $a \notin R(C(e))$, we
 808 let $C'(e) = C(e)$. Otherwise, $C(e) \in Q_W$. From the fact that $C \in \llbracket \lambda \rrbracket$, we deduce that
 809 $f(e) = x_{(p,k)}$, with $(p, k) \in \mathbf{lab}(\mathcal{S})$, and let $S = (\mathbf{pr}, p, k) \in \mathcal{S}$. Now,
 - 810 ■ if $(p, k) \notin \mathbf{lab}(\mathcal{S}')$, it means that $S \xrightarrow{t} \mathbf{Done}$, and we let $C'(e) = p$. By definition of
 811 $S \xrightarrow{t} \mathbf{Done}$, we deduce that $(C(e), ?a, p) \in T$.
 - 812 ■ Otherwise, let $S' = (\mathbf{pr}', p, k) \in \mathcal{S}'$. By definition of the VASS, it means that
 813 either $S \xrightarrow{t} S'$, or $S \xrightarrow{t, q'} S'$. In both cases, there exists a configuration C'' such
 814 that $\dot{q} \oplus \dot{\mathbf{pr}} \xrightarrow{1, t} \dot{q}' \oplus C''$ and either $\mathbf{set}(C'') = \mathbf{pr}'$ or $\mathbf{set}(C'') \cup \{q'\} = \mathbf{pr}'$. Let e'
 815 such that $\dot{q} \oplus \dot{\mathbf{pr}}(e') = C(e)$ (we will justify its existence just after), then, we define

816 $C'(e) = q' \oplus C''(e')$. Observe that we know that such a process e' exists, thanks to
 817 **CondImpl2**: since $e \in f^{-1}(x_{p,k})$, $C(e) \in \text{pr} \cup \{p\}$. Moreover, $C(e) \in Q_W$, hence
 818 $C(e) \in \text{pr}$. Then we know that $(C(e), ?a, C'(e)) \in T$ or $C(e) = C'(e)$ and $a \notin R(C(e))$.

819 It is clear from the construction of C' that $C \rightarrow C'$. We show now that $C' \in \llbracket \lambda' \rrbracket$ by
 820 defining $f' : [1, n] \rightarrow X$ as follows.

$$821 \quad f'(e_q) = \begin{cases} x_{q'} & \text{if } q' \in Q_A \\ x_{p,k} & \text{if } q' \in Q_W, \text{ and } \mathbf{lab}(S) = \mathbf{lab}(S'), \text{ with } x_{p,k} \text{ the unique counter such that } \delta(x_{p,k}) = 1 \\ x_{p,k} & \text{otherwise, with } (p, k) \in \mathbf{lab}(S') \setminus \mathbf{lab}(S) \end{cases}$$

$$822 \quad f'(e) = f(e) \quad \text{for all other } e \in [1, n]$$

823 We have to prove **CondImpl1**, **CondImpl2** and **CondImpl3**, but also that for all $x \in X$,
 824 $|f'^{-1}(x)| = v'(x)$.

825 Let $p \in Q_A$ and $e \neq e_q$ such that $f'(e) = p = f(e)$ by construction. Then $C(e) = p$, and
 826 by definition of C' , $C'(e) = p$. If e_q is such that $f'(e_q) = p$, it means that $p = q' \in Q_A$. In that
 827 case, $C'(e_q) = p$. In any case, **CondImpl1** is satisfied. Moreover, if $p \neq q'$, $v'(x_p) = v(x_p)$
 828 and $f'^{-1}(x_p) = f^{-1}(x_p)$, hence $|f'^{-1}(x_p)| = v'(x_p)$. If $p = q'$, then $v'(x_p) = v(x_p) + 1$ and
 829 $f'^{-1}(x_p) = f^{-1}(x_p) \cup \{e_q\}$ thus $|f'^{-1}(x_p)| = v'(x_p)$.

830 Let (p, k) such that $S' = (\text{pr}', p, k) \in S'$ with $(p, k) \in \mathbf{lab}(S)$. Let $e \in f'^{-1}(x_{p,k})$. If
 831 $e \neq e_q$, then $e \in f^{-1}(x_{p,k})$. Let $S = (\text{pr}, p, k) \in S$. Then $C(e) \in \text{pr} \cup \{p\}$. By construction
 832 of C' , either $C(e) = p = C'(e)$, or $C(e) \in \text{pr}$ and $C'(e) \in \text{pr}'$. If $e = e_q$, by definition
 833 of f' , $q' \in Q_W$, $\mathbf{lab}(S) = \mathbf{lab}(S')$, and $\delta(x_{p,k}) = 1$, and, by definition of the VASS, this
 834 means that $S \xrightarrow{t+q'} S'$, hence $q' \in \text{pr}'$. Hence, $C'(e) = q' \in \text{pr}'$. Let now (p, k) such that

835 $S' = (\text{pr}', p, k) \in S'$ with $(p, k) \notin \mathbf{lab}(S)$. Let $e \in f'^{-1}(x_{p,k})$ such that $e \neq e_q$. In that
 836 case, $e \in f^{-1}(x_{p,k})$. Since $(p, k) \notin \mathbf{lab}(S)$, by **CondImpl3**, $C(e) = p \in Q_A$, and then
 837 $C'(e) = p \in \text{pr}' \cup \{p\}$. If $e = e_q$, $C'(e) = q'$, and by definition of the VASS, $S' = (\{q'\}, p, k)$.

838 Then **CondImpl2** is met. Moreover, if $f'(e_q) = x_{p,k}$ then $|f'^{-1}(x_{p,k})| = |f^{-1}(x_{p,k})| + 1$,
 839 and in that case, $v'(x_{p,k}) = v(x_{p,k}) + 1$. Otherwise, $|f'^{-1}(x_{p,k})| = |f^{-1}(x_{p,k})| = v(x_{p,k}) =$
 840 $v'(x_{p,k})$.

841 Finally, let $(p, k) \notin \mathbf{lab}(S')$, and let $e \in f'^{-1}(x_{p,k})$. By definition of f' , $e \in f^{-1}(x_{p,k})$. If
 842 $(p, k) \notin \mathbf{lab}(S)$, then by **CondImpl3** on C , $C(e) = p \in Q_A$, and then $C'(e) = p$. Otherwise,
 843 let $S = (\text{pr}, p, k) \in S$, we must have $S \xrightarrow{t} \text{Done}$. By construction, $C'(e) = p$. Then

844 **CondImpl3** is satisfied by C' . Moreover, $v(x_{p,k}) = v'(x_{p,k})$ and $|f'^{-1}(x_{p,k})| = |f^{-1}(x_{p,k})|$.

845 We have hence proved that $C' \in \llbracket \lambda' \rrbracket$.
 846 ◀

847 **D** Proofs of Section 3.2

848 **Proof of Lemma 3.10.** Let $C = \rho_i$ and let $\lambda_i = (S, v) \in \text{repr}(\rho, i)$, then

849 ■ **CondRepr1** for all $q \in Q_A$, $v(x_q) = |C^{-1}(q)|$;

850 and for all $q_a \in Q_A$, there is an injective function $r_{q_a} : \text{na-index-set}(\rho, i, q_a) \rightarrow [1, |Q_W| + 1]$
 851 s.t.:

852 ■ **CondRepr2** $S = \{(C(E_{q_a, j}^{\rho, i}), q_a, r_{q_a}(j)) \mid q_a \in Q_A, j \in \text{na-index-set}(\rho, i, q_a)\}$, and,

853 **CondRepr3** for all $q_a \in Q_A$, we have $v(x_{q_a, r_{q_a}(j)}) = |E_{q_a, j}^{\rho, i}|$ for all $j \in \mathbf{na-index-set}(\rho, i, q_a)$
 854 and $v(x_{q_a, k}) = 0$ for all $k \notin r_{q_a}(\mathbf{na-index-set}(\rho, i, q_a))$.

855 Let $t = (q, !!m, q')$ and $e_q \in [1, \#\mathbf{proc}(\rho)]$ such that $C \xrightarrow{e_q, t} C_{i+1}$. We start by building
 856 an S -configuration $\lambda' = (S', v')$ such that $\lambda_i \rightsquigarrow^t \lambda'$. We will then show that an appropriate
 857 sequence of transitions from Δ_e allows to go from λ' to a configuration $\lambda_{i+1} \in \mathbf{repr}(\rho, i+1)$.

858 We state first some remarks about $E_{q_a, j}^{\rho, i}$, for $q_a \in Q_A$ and $j \in \mathbf{na-index-set}(\rho, i, q_a)$.
 859 Observe that, since $E_{q_a, j}^{\rho, i} = \{e \in [1, \#\mathbf{proc}(\rho)] \mid C(e) \subseteq Q_W \text{ and } \mathbf{na}(\rho, i, e) = (q_a, j)\}$, if
 860 $C_{i+1}(e) \in Q_A$ for some $e \in E_{q_a, j}^{\rho, i}$, then it means that $j = i+1$ and $C_{i+1}(e) = q_a$ for all
 861 $e \in E_{q_a, j}^{\rho, i}$. Hence $C_{i+1}(E_{q_a, j}^{\rho, i}) \subseteq Q_W$ or $C_{i+1}(E_{q_a, j}^{\rho, i}) = \{q_a\}$.

862 Observe also that if $j \in \mathbf{na-index-set}(\rho, i, q_a)$:

$$863 \quad E_{q_a, j}^{\rho, i+1} = \begin{cases} \emptyset & \text{if } j = i+1 \\ E_{q_a, j}^{\rho, i} \cup \{e_q\} & \text{if } q' \in Q_W \text{ and } \mathbf{na}(\rho, i+1, e_q) = (q_a, j) \\ E_{q_a, j}^{\rho, i} & \text{otherwise.} \end{cases}$$

864 Moreover, if $q' \in Q_W$, and $\mathbf{na}(\rho, i, e_q) = (q_a, j)$ with $j \notin \mathbf{na-index-set}(\rho, i, q_a)$, then
 865 $E_{q_a, j}^{\rho, i} = \emptyset$ hence $E_{q_a, j}^{\rho, i+1} = \{e_q\}$.

866 **Definition of λ' .**

867 For all $S = (\mathbf{pr}, q_a, k) \in \mathcal{S}$, we let $j = r_{q_a}^{-1}(k)$ and we define

$$868 \quad S^{+1} = \begin{cases} (C_{i+1}(E_{q_a, j}^{\rho, i}), q_a, k) & \text{if } C_{i+1}(E_{q_a, j}^{\rho, i}) \subseteq Q_W \\ \text{Done} & \text{otherwise.} \end{cases}$$

869 Recall that $t = (q, !!m, q')$. S^{+1} represents the set of states reached by the states in the
 870 print of S after the sending of the message m in ρ . We then let $\mathcal{S}_0 = \{S^{+1} \mid S \in \mathcal{S}\} \setminus \{\text{Done}\}$.

871 We first show that $\mathcal{S}_0$ is coherent: let $S_1 = (\mathbf{pr}_1, q_a, k_1), S_2 = (\mathbf{pr}_2, q_a, k_2) \in \mathcal{S}$ such that
 872 $S_1^{+1} = (\mathbf{pr}'_1, q_a, k_1)$ and $S_2^{+1} = (\mathbf{pr}'_2, q_a, k_2)$ are two distinct summaries in $\mathcal{S}_0$. Since $\mathcal{S}$ is
 873 coherent, we know that $k_1 \neq k_2$. Assume that there is some $p \in \mathbf{pr}'_1 \cap \mathbf{pr}'_2$. It means that
 874 there exist $e_1, e_2 \in [1, \#\mathbf{proc}(\rho)]$ such that $C_{i+1}(e_1) = p = C_{i+1}(e_2)$, and $e_1 \in E_{q_a, r_{q_a}^{-1}(k_1)}^{\rho, i}$
 875 and $e_2 \in E_{q_a, r_{q_a}^{-1}(k_2)}^{\rho, i}$. Hence by definition, $\mathbf{na}(\rho, i, e_1) = (q_a, r_{q_a}^{-1}(k_1)) = \mathbf{na}(\rho, i+1, e_1)$
 876 and $\mathbf{na}(\rho, i, e_2) = (q_a, r_{q_a}^{-1}(k_2)) = \mathbf{na}(\rho, i+1, e_2)$. Since ρ is well-formed, we have that
 877 $r_{q_a}^{-1}(k_1) = r_{q_a}^{-1}(k_2)$, and hence, $k_1 = k_2$, which is a contradiction. Then for any two distinct
 878 summaries $S_1^{+1} = (\mathbf{pr}'_1, q_a, k_1)$ and $S_2^{+1} = (\mathbf{pr}'_2, q_a, k_2)$, then $k_1 \neq k_2$ and $\mathbf{pr}'_1 \cap \mathbf{pr}'_2 = \emptyset$.

879 Now that we have defined $\mathcal{S}_0$, which captures the behavior of the processes that were
 880 in waiting states when the transition t has been taken, we define S' that incorporates the
 881 process e_q that arrives in state q' , along with the new valuation v' . In particular, if $q' \in Q_W$,
 882 then e_q reaches a zone of waiting states and must enter one of the summaries. Observe first
 883 that $C(e_q) = q$, then $v(x_q) = |C^{-1}(q)| > 0$, then we let $v'(x_q) = v(x_q) - 1$.

884 **■** If $q' \in Q_A$, $S' = \mathcal{S}_0$ and $v'(x_{q'}) = v(x_{q'}) + 1$. For all other counter x , $v'(x) = v(x)$.

885 **■** If $q' \in Q_W$, let $\mathbf{na}(\rho, i+1, e_q) = (p, j)$.

886 1. If $j \in \mathbf{na-index-set}(\rho, i, p)$, then e_q will join an existing summary $S = (\mathbf{pr}, p, r_p(j)) \in \mathcal{S}$.
 887 Since $\mathbf{na}(\rho, i+1, e_q) = (p, j)$ and $q' \in Q_W$, $j \neq i+1$ and $C_{i+1}(E_{q_a, j}^{\rho, i}) \subseteq Q_W$. Hence,
 888 $S^{+1} \neq \text{Done}$. We then let $S' = (\mathcal{S}_0 \setminus \{S^{+1}\}) \cup \{(\mathbf{print}(S^{+1}) \cup \{q'\}, p, r_p(j))\}$. Then
 889 $v'(x_{p, r_p(j)}) = v(x_{p, r_p(j)}) + 1$ and $v'(x) = v(x)$ for all other counter x .

2. If $j \notin \mathbf{na-index-set}(\rho, i, p)$, then e_q will enter a new summary. Observe first that $\mathbf{na-index-set}(\rho, i+1, p) = (\mathbf{na-index-set}(\rho, i, p) \cup \{j\}) \setminus \{i+1\}$. So $|\mathbf{na-index-set}(\rho, i, p)| \leq |\mathbf{na-index-set}(\rho, i+1, p)| \leq |Q_W| < 1 + |Q_W|$, and there exists $k \in [1, |Q_W| + 1]$ such that $k \notin r_p(\mathbf{na-index-set}(\rho, i, p))$. This means that (p, k) is a label that is not used in the summaries of $\mathcal{S}_0$ and we let $\mathcal{S}' = \mathcal{S}_0 \cup \{(\{q'\}, p, k)\}$. Now, $v'(x_{p,k}) = v(x_{p,k}) + 1$, and for all other counter x , $v'(x) = v(x)$.

Proof that $\lambda' = (\mathcal{S}', v')$ is an S -configuration.

To prove that λ' is an S -configuration, we need to show that $\mathcal{S}'$ is coherent. The definition of $\mathcal{S}'$ depends on the belonging of q' to Q_A or Q_W .

- If $q' \in Q_A$, then $\mathcal{S}' = \mathcal{S}_0$, and we have proved above that $\mathcal{S}_0$ is coherent.
- if $q' \in Q_W$, let $S_1 = (\mathbf{pr}_1, q_a, k_1)$ and $S_2 = (\mathbf{pr}_2, q_a, k_2) \in \mathcal{S}_1$, for some $q_a \in Q_A$. If $S_1, S_2 \in \mathcal{S}_0$, by coherence of $\mathcal{S}_0$, we have that $k_1 \neq k_2$ and $\mathbf{pr}_1 \cap \mathbf{pr}_2 = \emptyset$. Otherwise, assume that $S_2 \in \mathcal{S}' \setminus \mathcal{S}_0$. Depending on whether e_q has joined an existing summary or a new one, $S_2 = (\mathbf{pr} \cup \{q'\}, p, k_2)$ with $\mathbf{pr}$ empty or not. Then $S_1 = (\mathbf{pr}_1, p, k_1)$ and if $\mathbf{pr} \neq \emptyset$, $(\mathbf{pr}, p, k_2) \in \mathcal{S}_0$. By coherence of $\mathcal{S}_0$, $k_1 \neq k_2$ and $\mathbf{pr}_1 \cap \mathbf{pr} = \emptyset$. Moreover, if $S_2 = (\{q'\}, p, k_2)$, by construction, $k_2 \neq k_1$. Assume now that there exists $p' \in \mathbf{pr}_1 \cap \mathbf{pr}_2$. Then $p' = q'$, and there exists $e_1 \in [1, \#\mathbf{proc}(\rho)]$ such that $C_{i+1}(e_1) = q'$ and $\mathbf{na}(\rho, i, e_1) = (p, r_p^{-1}(k_1))$. But $\mathbf{na}(\rho, i, e_q) = (p, j)$. Since ρ is well-formed, $j = r_p^{-1}(k_1)$, which is impossible because $k_2 \neq k_1$. Hence $\mathbf{pr}_1 \cap \mathbf{pr}_2 = \emptyset$, and $\mathcal{S}_1$ is coherent.

Proof that $\lambda_i \xrightarrow{t} \lambda'$.

We first show that for all $S \in \mathcal{S}$, $S \xrightarrow{t} S^{+1}$. Let $S = (\mathbf{pr}, q_a, k) \in \mathcal{S}$ with $j' = r_{q_a}^{-1}(k)$. By construction, $\mathbf{pr} = C(E_{q_a, j'}^{\rho, i})$. Assume now that there exist $e_1, e_2 \in E_{q_a, j'}^{\rho, i}$ such that $C(e_1) = C(e_2)$. Since ρ is well-defined, $C_{i+1}(e_1) = C_{i+1}(e_2)$. Then, for all $s \in \mathbf{pr}$, there exists a unique $s^{+1} \in Q$ such that $C_{i+1}(e) = s^{+1}$ for all $e \in C_i^{-1}(s)$. Moreover, $C_{i+1}(E_{q_a, j'}^{\rho, i}) = \{s^{+1} \mid s \in \mathbf{pr}\}$. We define a configuration C' as follows. Let $e \in [1, |\mathbf{pr}|]$ such that $\mathbf{pr}(e) = p_e \in \mathbf{pr}$. Then $C'(e) = p_e^{+1}$. Hence $\mathbf{set}(C') = \{s^{+1} \mid s \in \mathbf{pr}\} = C_{i+1}(E_{q_a, j'}^{\rho, i})$. We show now that $\dot{q} \oplus 1, \mathbf{pr} \xrightarrow{t} \dot{q}' \oplus C'$. Let $e \in [1, |\mathbf{pr}|]$ such that $\mathbf{pr}(e) = p_e$, then there exists $e' \in E_{q_a, j'}^{\rho, i}$ such that $C(e') = p_e$ and $p_e^{+1} = C_{i+1}(e')$. Since $C \xrightarrow{t} C_{i+1}$, then either $(p_e, ?m, p_e^{+1}) \in T$ or $m \notin R(p_e)$ and $p_e = p_e^{+1}$, and $\dot{q} \oplus \mathbf{pr} \xrightarrow{1, t} \dot{q}' \oplus C'$. We have already established that $\mathbf{set}(C') = C_{i+1}(E_{q_a, j'}^{\rho, i}) \subseteq Q_W$ or $\mathbf{set}(C') = C_{i+1}(E_{q_a, j'}^{\rho, i}) = \{q_a\}$. In the latter case, $S^{+1} = \text{Done}$. In the former case, $S^{+1} = (C_{i+1}(E_{q_a, j'}^{\rho, i}), q_a, k)$ and $S \xrightarrow{t} S^{+1}$. In any case, we have that $S \xrightarrow{t} S^{+1}$.

Now we show that there exists δ such that $\lambda_i \xrightarrow{\delta} \lambda'$ and $(\mathcal{S}, \delta, \mathcal{S}') \in \Delta_t$.

- If $q' \in Q_A$, we must have $\delta(x_q) = -1$, $\delta(x_{q'}) = 1$ and for all other counter x , $\delta(x) = 0$. Observe that we have $v'(x_q) = v(x_q) - 1$, $v'(x_{q'}) = v(x_{q'}) + 1$, and $v'(x) = v(x)$ for all other counter x . Moreover, $\mathcal{S}' = \mathcal{S}_0 = \{S^{+1} \mid S \in \mathcal{S}\} \setminus \{\text{Done}\}$. Since $S \xrightarrow{t} S^{+1}$ for all $S \in \mathcal{S}$, we have $(\mathcal{S}, \delta, \mathcal{S}') \in \Delta_t$ and $\lambda_i \xrightarrow{\delta} \lambda'$.
- If $q' \in Q_W$ and there exists some $S = (\mathbf{pr}, p, r_p(j))$ such that $\mathcal{S}' = (\mathcal{S}_0 \setminus \{S^{+1}\}) \cup \{(\mathbf{print}(S^{+1}) \cup \{q'\}, p, r_p(j))\}$. Then $v'(x_q) = v(x_q) - 1$, $v'(x_{p, r_p(j)}) = v(x_{p, r_p(j)}) + 1$ and $v'(x) = v(x)$ for all other counter x . By definition, $S \xrightarrow{t, q'} (\mathbf{print}(S^{+1}) \cup \{q'\}, p, r_p(j))$,

930 because $S \xRightarrow{t} S^{+1}$. Then, with $\delta(x_q) = -1$, $\delta(x_{p,r_p(j)}) = 1$ and $\delta(x) = 0$ for all other
 931 counter, we have $(\mathcal{S}, \delta, \mathcal{S}') \in \Delta_t$, and $\lambda_i \xrightarrow{\delta} \lambda'$.
 932 ■ If $q' \in Q_W$ and $\mathcal{S}' = \mathcal{S}_0 \cup \{(\{q'\}, p, k)\}$ for some $k \in [1, |Q_W| + 1]$. Then $(p, k) \notin \mathbf{lab}(\mathcal{S})$
 933 by construction. Moreover, $v'(x_q) = v(x_q) - 1$, and $v'(x_{p,k}) = 1$. For all other counter
 934 x , $v'(x) = v(x)$. For δ such that $\delta(x_q) = -1$, $\delta(x_{p,k}) = 1$ and $\delta(x) = 0$ for all other x ,
 935 $(\mathcal{S}, \delta, \mathcal{S}') \in \Delta_t$ and $\lambda_i \xrightarrow{\delta} \lambda'$.
 936 Then $\lambda_i \xrightarrow{t} \lambda'$.

937 **Definition of λ_{i+1} .**

938 First observe that for all $q_a \in Q_A$, there is at most one $S = (\mathbf{pr}, q_a, k) \in \mathcal{S}$ such that
 939 $S^{+1} = \text{Done}$. Indeed, suppose there exist $S_1 = (\mathbf{pr}_1, q_a, k_1), S_2 = (\mathbf{pr}_2, q_a, k_2) \in \mathcal{S}$ such that
 940 $S_1^{+1} = S_2^{+1} = \text{Done}$. By definition, $\mathbf{pr}_1 = C(E_{q_a, r_{q_a}^{-1}(k_1)}^{\rho, i})$ and $\mathbf{pr}_2 = C(E_{q_a, r_{q_a}^{-1}(k_2)}^{\rho, i})$. Let
 941 $j_1 = r_{q_a}^{-1}(k_1)$ and $j_2 = r_{q_a}^{-1}(k_2)$. Then, since $C_{i+1}(E_{q_a, j_i}^{\rho, i}) \notin Q_W$ for $i \in \{1, 2\}$, necessarily
 942 $C_{i+1}(E_{q_a, j_1}^{\rho, i}) = C_{i+1}(E_{q_a, j_2}^{\rho, i}) = \{q_a\}$. This implies that $j_1 = j_2 = i + 1$ and then that $k_1 = k_2$,
 943 which contradicts the coherence of $\mathcal{S}$. This means that, there is at most one summary for
 944 each $q_a \in Q_A$ that disappears while going from $\mathcal{S}$ to $\mathcal{S}'$. This corresponds to a set of processes
 945 leaving a waiting zone and reaching the state q_a together. We capture this new configuration
 946 of the protocol by transferring the value of the counter $x_{(q_a, r_{q_a}(i+1))}$ into the counter x_{q_a} .

947 So we define v_{i+1} as follows. For all $q_a \in Q_A$,

- 948 ■ if $i + 1 \in \mathbf{na-index-set}(\rho, i, q_a)$, let $k = r_{q_a}(i + 1)$ and $v_{i+1}(x_{q_a}) = v'(x_{q_a}) + v'(x_{q_a, k})$ and
 949 $v_{i+1}(x_{q_a, k}) = 0$.
- 950 ■ Otherwise, $v_{i+1}(x_{q_a}) = v'(x_{q_a})$.

951 For all other counter x , $v_{i+1}(x) = v'(x)$.

952 We let $\lambda_{i+1} = (\mathcal{S}', v_{i+1})$.

953 **Proof that $\lambda_{i+1} \in \mathbf{repr}(\rho, i + 1)$.**

954 We show that **CondRepr1** holds. Let $q_a \in Q_A$. Observe first that, for all $e \in [1, \#\mathbf{proc}(\rho)]$,
 955 $(C(e), ?m, q_a) \in T$ if and only if $e \in E_{q_a, i+1}^{\rho, i}$. Let $e_q \in [1, \#\mathbf{proc}(\rho)]$ such that $C \xrightarrow{e_q, t} C_{i+1}$.
 956 Then, $C(e_q) = q$, $C_{i+1}(e_q) = q'$. For all $e \in [1, \#\mathbf{proc}(\rho)]$, $C_{i+1}(e) = q_a$ if and only if
 957 $(C(e) = q_a$ and $q \neq q_a$, or $C(e) = q_a$ and $e \neq e_q$, or $(C(e), ?m, q_a) \in T$ and $\mathbf{na}(\rho, i, e) =$
 958 $(q_a, i + 1)$, or $q_a = q'$ and $e = e_q)$. From this we can deduce that

$$959 \quad C_{i+1}^{-1}(q_a) = \begin{cases} C^{-1}(q_a) \cup E_{q_a, i+1}^{\rho, i} & \text{if } q_a \neq q \text{ and } q_a \neq q' \\ C^{-1}(q_a) \cup \{e_q\} \cup E_{q_a, i+1}^{\rho, i} & \text{if } q_a = q' \\ (C^{-1}(q_a) \setminus \{e_q\}) \cup E_{q_a, i+1}^{\rho, i} & \text{if } q_a = q. \end{cases}$$

960 Since $C^{-1}(q_a)$ and $E_{q_a, i+1}^{\rho, i}$ are disjoint, and if $e_q \in C^{-1}(q) \setminus C^{-1}(q')$ (which is always
 961 true as $q \neq q'$ by hypothesis), we have that

$$962 \quad |C_{i+1}^{-1}(q_a)| = \begin{cases} |C^{-1}(q_a)| + |E_{q_a, i+1}^{\rho, i}| & \text{if } q_a \neq q \text{ and } q_a \neq q' \\ |C^{-1}(q_a)| + 1 + |E_{q_a, i+1}^{\rho, i}| & \text{if } q_a = q' \\ |C^{-1}(q_a)| - 1 + |E_{q_a, i+1}^{\rho, i}| & \text{if } q_a = q. \end{cases}$$

963 Since $\lambda = (\mathcal{S}, v) \in \mathbf{repr}(C, i)$, $v(x_{q_a}) = |C^{-1}(q_a)|$ and, by definition of v' , we have that

$$964 |C_{i+1}^{-1}(q_a)| = v'(x_{q_a}) + |E_{q_a, i+1}^{\rho, i}|$$

965 Now, if $i + 1 \notin \mathbf{na-index-set}(\rho, i, q_a)$, it means that $E_{q_a, i+1}^{\rho, i} = \emptyset$. In that case, by definition of v_{i+1} , $v_{i+1}(x_{q_a}) = v'(x_{q_a}) = |C_{i+1}^{-1}(q_a)|$.

966 Otherwise, $v_{i+1}(x_{q_a}) = v'(x_{q_a}) + v'(x_{(q_a, r_{q_a}(i+1))})$. The only case where $v'(x_{(q_a, r_{q_a}(i+1))}) \neq v(x_{(q_a, r_{q_a}(i+1))})$ is if $q' \in Q_W$ and $\mathbf{na}(\rho, i + 1, e_q) = (q_a, i + 1)$ which is impossible. So
967 $v'(x_{(q_a, r_{q_a}(i+1))}) = v(x_{(q_a, r_{q_a}(i+1))})$ and, since $\lambda \in \mathbf{repr}(C, i)$, $v(x_{(q_a, r_{q_a}(i+1))}) = |E_{q_a, i+1}^{\rho, i}|$. Hence,
968 $v_{i+1}(x_{q_a}) = v'(x_{q_a}) + |E_{q_a, i+1}^{\rho, i}| = |C_{i+1}^{-1}(q_a)|$, proving **CondRepr1**.

969 We now show that **CondRepr3** holds. Let $q_a \in Q_A$. We define an injective function
970 $r'_{q_a} : \mathbf{na-index-set}(\rho, i + 1, q_a) \rightarrow [1, |Q_W| + 1]$ as follows.
971 Recall that

$$\mathbf{na-index-set}(\rho, i + 1, q_a) = \begin{cases} (\mathbf{na-index-set}(\rho, i, q_a) \setminus \{i + 1\}) \cup \{\mathbf{na-index}(\rho, i + 1, e_q)\} & \text{if } q' \in Q_W \text{ and } \mathbf{na-state}(\rho, i + 1, e_q) = q_a \\ \mathbf{na-index-set}(\rho, i, q_a) \setminus \{i + 1\} & \text{otherwise.} \end{cases}$$

973 The new injective function keeps the labelling of the summaries as in the previous
974 configuration, and if needed, associates the labelling of the newly created summary to the
975 index of the next moment e_q will reach an action state.

$$976 r'_{q_a}(j) = \begin{cases} r_{q_a}(j) & \text{if } j \in \mathbf{na-index-set}(\rho, i, q_a) \setminus \{i + 1\} \\ k & \text{otherwise, for the unique } k \text{ such that } (\{q'\}, q_a, k) \in \mathcal{S}' \text{ and } (q_a, k) \notin \mathbf{lab}(\mathcal{S}) \end{cases}$$

977 ■ For $j \in \mathbf{na-index-set}(\rho, i, q_a) \setminus \{i + 1\}$, $v_{i+1}(x_{q_a, r'_{q_a}(j)}) = v_{i+1}(x_{q_a, r_{q_a}(j)}) = v'(x_{q_a, r_{q_a}(j)})$.
978 By definition of v' ,

$$979 v'(x_{q_a, r_{q_a}(j)}) = \begin{cases} v(x_{q_a, r_{q_a}(j)}) + 1 & \text{if } q' \in Q_W \text{ and } \mathbf{na}(\rho, i + 1, e_q) = (q_a, j) \\ v(x_{q_a, r_{q_a}(j)}) & \text{otherwise.} \end{cases}$$

980 We know that $v(x_{q_a, r_{q_a}(j)}) = |E_{q_a, j}^{\rho, i}|$. Moreover,

$$981 E_{q_a, j}^{\rho, i+1} = \begin{cases} E_{q_a, j}^{\rho, i} \cup \{e_q\} & \text{if } q' \in Q_W \text{ and } \mathbf{na}(\rho, i + 1, e_q) = (q_a, j) \\ E_{q_a, j}^{\rho, i} & \text{otherwise.} \end{cases}$$

982 Hence, $v_{i+1}(x_{q_a, r'_{q_a}(j)}) = v'(x_{q_a, r_{q_a}(j)}) = |E_{q_a, j}^{\rho, i+1}|$.

983 ■ If $\mathbf{na}(\rho, i + 1, e_q) = (q_a, j)$ and $j \notin \mathbf{na-index-set}(\rho, i, q_a)$, by definition $v_{i+1}(x_{q_a, r'_{q_a}(j)}) = v'(x_{q_a, r'_{q_a}(j)}) = v'(x_{q_a, k})$. This corresponds to the case 2 of the definition of v' , where
984 $v'(x_{q_a, k}) = v(x_{q_a, k}) + 1$. By definition of k , $k \notin r_p(\mathbf{na-index-set}(\rho, i, q_a))$, then, since
985 $\lambda = (\mathcal{S}, v) \in \mathbf{repr}(C, i)$, $v(x_{q_a, k}) = 0$ and $v_{i+1}(x_{q_a, r'_{q_a}(j)}) = 1$. Moreover, $E_{q_a, j}^{\rho, i} = \{e \in [1, \#\mathbf{proc}(\rho)] \mid C(e) \in Q_W \text{ and } \mathbf{na}(\rho, i, e) = (q_a, j)\}$; since $j \notin \mathbf{na-index-set}(\rho, i, q_a)$,
986 $E_{q_a, j}^{\rho, i} = \emptyset$. Hence, $E_{q_a, j}^{\rho, i+1} = E_{q_a, j}^{\rho, i} \cup \{e_q\} = \{e_q\}$ and $v_{i+1}(x_{q_a, r'_{q_a}(j)}) = 1 = |E_{q_a, j}^{\rho, i+1}|$.
987
988

989 Let $x \notin r'_{q_a}(\mathbf{na-index-set}(\rho, i + 1, q_a))$. Then, either $x \notin \mathbf{na-index-set}(\rho, i, q_a)$, or
990 $x = r_{q_a}(i + 1)$. In the first case, $v(x_{q_a, x}) = 0$, and $v_{i+1}(x_{q_a, x}) = v'(x_{q_a, x}) = v(x_{q_a, x}) = 0$. In
991 the second case, $v_{i+1}(x_{q_a, x}) = 0$ by construction.

992 Finally we show that **CondRepr2** holds, i.e. that $\mathcal{S}' = \{(C_{i+1}(E_{q_a, j}^{\rho, i+1}), q_a, r'_{q_a}(j)) \mid q_a \in Q_A, j \in \mathbf{na-index-set}(\rho, i + 1, q_a)\}$.
993

- 994 ■ First, let $S \in \mathcal{S}'$. By construction, either $S \in \mathcal{S}_0$, or there exists $S_0 = (\text{pr}, q_a, r_{q_a}(j)) \in \mathcal{S}_0$
 995 such that $S = (\text{pr} \cup \{q'\}, q_a, r_{q_a}(j))$, or $S = (\{q'\}, q_a, k)$.
- 996 ■ If $S \in \mathcal{S}_0$ and $\text{na}(\rho, i, q_a, e_q) \neq (q_a, j)$ or $q' \in Q_A$, then $S = S_0^{+1}$ for some $S_0 \in$
 997 $\mathcal{S}$. Since $(\mathcal{S}, v) \in \text{repr}(C, i)$, $S_0 = (C(E_{q_a, j}^{\rho, i}), q_a, r_{q_a}(j))$ for some $q_a \in Q_A$ and
 998 $j \in \text{na-index-set}(\rho, i, q_a)$. Then $S = (C_{i+1}(E_{q_a, j}^{\rho, i}), q_a, r_{q_a}(j))$. Then $E_{q_a, j}^{\rho, i+1} =$
 999 $E_{q_a, j}^{\rho, i}$. Moreover, $S_0^{+1} \neq \text{Done}$ hence $j \neq i+1$, then $r'_{q_a}(j) = r_{q_a}(j)$. Then
 1000 $S = (C_{i+1}(E_{q_a, j}^{\rho, i+1}), q_a, r'_{q_a}(j))$.
- 1001 ■ If there exists $S_0 = (\text{pr}, q_a, r_{q_a}(j)) \in \mathcal{S}$ such that $S = (\text{pr} \cup \{q'\}, q_a, r_{q_a}(j))$ and $q' \in Q_W$
 1002 and $\text{na}(\rho, i+1, e_q) = (q_a, j)$, then, in that case, $E_{q_a, j}^{\rho, i+1} = E_{q_a, j}^{\rho, i} \cup \{e_q\}$ and $j \neq i+1$ so
 1003 $r'_{q_a}(j) = r_{q_a}(j)$. Then $S = (C_{i+1}(E_{q_a, j}^{\rho, i+1}), q_a, r'_{q_a}(j))$.
- 1004 ■ Finally, if $S = (\{q'\}, q_a, k)$, and $q' \in Q_W$, with $\text{na}(\rho, i+1, e_q) = (q_a, j)$ and $j \notin$
 1005 $\text{na-index-set}(\rho, i, q_a)$, then $E_{q_a, j}^{\rho, i+1} = \{e_q\}$ and $r'_{q_a}(j) = k$. Then, $S = (C_{i+1}(E_{q_a, j}^{\rho, i+1}), q_a, r'_{q_a}(j))$.
- 1006 ■ Now, let $S = (C_{i+1}(E_{q_a, j}^{\rho, i+1}), q_a, r'_{q_a}(j))$ for some $q_a \in Q_A$, $j \in \text{na-index-set}(\rho, i+1, q_a)$.
 1007 ■ If $j \in \text{na-index-set}(\rho, i, q_a)$, then $j \neq i+1$ and $r'_{q_a}(j) = r_{q_a}(j)$. Let $S_0 =$
 1008 $(C(E_{q_a, j}^{\rho, i}), q_a, r_{q_a}(j)) \in \mathcal{S}$. Such a summary exists because $\lambda = (\mathcal{S}, v)$ is in $\text{repr}(C, i)$.
 1009 If $q' \in Q_W$ and $\text{na}(\rho, i+1, e_q) = (q_a, j)$, then, $E_{q_a, j}^{\rho, i+1} = E_{q_a, j}^{\rho, i} \cup \{e_q\}$. Moreover, by con-
 1010 struction of $\mathcal{S}'$, $(C_{i+1}(E_{q_a, j}^{\rho, i}) \cup \{q'\}, q_a, r_{q_a}(j)) = (C_{i+1}(E_{q_a, j}^{\rho, i}) \cup C_{i+1}(e_q), q_a, r_{q_a}(j)) =$
 1011 $(C_{i+1}(E_{q_a, j}^{\rho, i+1}), q_a, r'_{q_a}(j)) \in \mathcal{S}'$. Otherwise, $E_{q_a, j}^{\rho, i+1} = E_{q_a, j}^{\rho, i}$ and, by construction of $\mathcal{S}'$,
 1012 $S_0^{+1} = (C_{i+1}(E_{q_a, j}^{\rho, i}), q_a, r_{q_a}(j)) = (C_{i+1}(E_{q_a, j}^{\rho, i+1}), q_a, r'_{q_a}(j)) \in \mathcal{S}'$.
- 1013 ■ If $j \notin \text{na-index-set}(\rho, i, q_a)$, then $r'_{q_a}(j) = k$ and $(\{q'\}, q_a, k) \in \mathcal{S}'$. Moreover, since
 1014 $j \in \text{na-index-set}(\rho, i+1, q_a) \setminus \text{na-index-set}(\rho, i, q_a)$, it means that $q' \in Q_W$ and
 1015 $\text{na}(\rho, i+1, e_q) = (q_a, j)$. Then $E_{q_a, j}^{\rho, i+1} = \{e_q\}$ and $S = (C_{i+1}(E_{q_a, j}^{\rho, i+1}), q_a, r'_{q_a}(j)) =$
 1016 $(\{q'\}, q_a, k) \in \mathcal{S}'$.

1017 **Proof that $\lambda' \rightsquigarrow^* \lambda_{i+1}$.**

1018 The only difference between λ' and λ_{i+1} is in the valuation. We show that $\lambda' \rightsquigarrow^* \lambda_{i+1}$
 1019 by successive transitions from Δ_e in order to transfer the values of counters associated to
 1020 summaries that have reached Done to the counter associated to the corresponding action
 1021 state.

1022 Let $\{q_1, \dots, q_n\} \subseteq Q_A$ such that, for all $1 \leq \ell \leq n$, $i+1 \in \text{na-index-set}(\rho, i, q_\ell)$. It is
 1023 the set of action states that are reached by processes in waiting zones in C_{i+1} . We build
 1024 the sequence of valuations $v_0, v_1, \dots, v_n$ with $v_0 = v'$ and $v_n = v_{i+1}$ such that $(\mathcal{S}', v_\ell) \rightsquigarrow^*$
 1025 $(\mathcal{S}', v_{\ell+1})$, for all $1 \leq \ell < n$. For all $1 \leq \ell \leq n$, let $k_\ell = r_{q_\ell}(i+1)$. We prove by induction
 1026 that, for all $1 \leq \ell \leq n$, $v_\ell(x_{q_{\ell'}, k_{\ell'}}) = v_{i+1}(x_{q_{\ell'}, k_{\ell'}})$, and $v_\ell(x_{q_{\ell'}}) = v_{i+1}(x_{q_{\ell'}})$ for $1 \leq \ell' \leq \ell$,
 1027 and for all other counter $v_\ell(x) = v'(x)$.

1028 Observe that for all $1 \leq \ell \leq n$, $k_\ell \notin r'_{q_\ell}(\text{na-index-set}(\rho, i+1, q_\ell))$. Indeed, by construc-
 1029 tion, if $\text{na}(\rho, i+1, e_q) = (q_\ell, j)$, then $r'_{q_\ell}(j) \neq r_{q_\ell}(i+1)$.

1030 Let $v(x_{q_1, k_1}) = x$. Then $(\mathcal{S}', v)(\rightsquigarrow^x_{\delta})^x(\mathcal{S}', v_1)$ with $\delta(x_{q_1}) = +1$, $\delta(x_{q_1, k_1}) = -1$ and
 1031 $\delta(x) = 0$ for all other x . Since $k_1 \notin r'_{q_1}(\text{na-index-set}(\rho, i+1, q_1))$, $(q_1, k_1) \notin \text{lab}(\mathcal{S}')$ (due
 1032 to the fact that $(\mathcal{S}', v_{i+1})$ is in $\text{repr}(\rho, i+1)$), and $(\mathcal{S}', \delta, \mathcal{S}') \in \Delta_e$. Moreover, $v_1(x_{q_1}) =$
 1033 $v'(x_{q_1}) + v'(x_{q_1, k_1}) = v_{i+1}(x_{q_1})$ and $v_1(x_{q_1, k_1}) = 0 = v_{i+1}(x_{q_1, k_1})$.

1034 Let $\ell \geq 1$ and assume that the property is true for $1 \leq \ell' < \ell$. We can show in a
 1035 similar way that for $\delta(x_{q_\ell}) = +1$, $\delta(x_{q_\ell, k_\ell}) = -1$ and $\delta(x) = 0$ for all other counter x , we
 1036 have $(\mathcal{S}', \delta, \mathcal{S}') \in \Delta_e$ and $(\mathcal{S}', v_{\ell-1})(\rightsquigarrow^{x'}_{\delta})^{x'}(\mathcal{S}', v_\ell)$, with $x' = v(x_{q_\ell, k_\ell})$. Hence, $v_\ell(x_{q_\ell}) =$
 1037 $v_{\ell-1}(x_{q_\ell}) + v_{\ell-1}(x_{q_\ell, k_\ell})$. By induction hypothesis, $v_\ell(x_{q_\ell}) = v'(x_{q_\ell}) + v'(x_{q_\ell, k_\ell}) = v_{i+1}(x_{q_\ell})$.

Moreover $v_\ell(x_{q_\ell, k_\ell}) = 0 = v_{i+1}(x_{q_\ell, k_\ell})$. For all other counter x , $v_\ell(x) = v_{\ell-1}(x)$, which proves the property.

Thus, we have proved that $(\mathcal{S}', v') \rightsquigarrow^* (\mathcal{S}', v_n)$ with $v_n(x_{q_\ell, k_\ell}) = v_{i+1}(x_{q_\ell, k_\ell})$, $v_n(x_{q_\ell}) = v_{i+1}(x_{q_\ell})$ for all $1 \leq \ell \leq n$, and $v_n(x) = v'(x)$ for all other counter x , hence $v_n = v_{i+1}$. $\blacktriangleleft$

E Proofs of Section 3.3

The proof of Theorem 3.13 relies on the following lemma.

► **Lemma E.1.** *There exist $C_0 \in \mathcal{I}$ and $C_f \in \mathcal{C}$ such that $C_0 \rightarrow^* C_f$ in P_V and $C_f(e) = q_f$ for all $e \in [1, ||C_f||]$ if and only if $(\ell_0, \mathbf{0}) \rightsquigarrow^* (\ell_f, \mathbf{0})$ in $\mathcal{V}$.*

Proof of Lemma E.1. Let the run of the VASS be $(\ell_0, v_0) \rightsquigarrow (\ell_1, v_1) \rightsquigarrow \dots \rightsquigarrow (\ell_n, v_n)$ with $v_0 = v_n = \mathbf{0}$ and $\ell_n = \ell_f$. We denote $K = \max_{0 \leq i \leq n} |v_i|$ where $|v_i| = \sum_{x \in X} v_i(x)$.

We build an execution of the protocol starting from the initial configuration C_0 with $K+1$ processes. The first part of the execution goes as follows.

$C_0 \xrightarrow{1, (q_{in}, !!\$, q_1)} C_1 \xrightarrow{2, (q_{in}, !!\$, q_1)} \dots \xrightarrow{K, (q_{in}, !!\$, q_1)} C_K \xrightarrow{K+1, (q_{in}, !!start, \ell_0)} C_{K+1}$. Observe that $C_{K+1}(j) = \text{zero}$ for all $1 \leq j \leq K$ and $C_{K+1}(K+1) = \ell_0$.

We rename C_{K+1} to C'_0 .

For all $0 \leq i \leq n$, let $\mathbb{C}(\ell_i, v_i) = \{C \in \mathcal{C} \mid C(K+1) = \ell_i, C([1, K]) = \{\text{zero}, \text{unit}_x \mid x \in X\}, \text{ and for all } x \in X, |C^{-1}(\text{unit}_x)| = v_i(x)\}$ be the set of protocol configurations corresponding to (ℓ_i, v_i) . We show by induction on i that, for all $0 \leq i \leq n$, for all $C \in \mathbb{C}(\ell_i, v_i)$, $C'_0 \rightarrow^* C$.

For $i = 0$, observe that $\mathbb{C}(\ell_0, v_0) = \{C'_0\}$ hence the property trivially holds. Let $0 \leq i < n$ and $(\ell_i, \delta_i, \ell_{i+1}) \in \Delta$ such that $(\ell_i, v_i) \rightsquigarrow (\ell_{i+1}, v_{i+1})$ and $v_{i+1} = v_i + \delta_i$. Let x_i be the unique counter such that $\delta_i(x_i) \in \{1, -1\}$. Let now $C \in \mathbb{C}(\ell_{i+1}, v_{i+1})$.

Case $\delta_i(x_i) = 1$. Then $v_{i+1}(x_i) = v_i(x_i) + 1 > 0$, and since $C \in \mathbb{C}(\ell_{i+1}, v_{i+1})$, there exists $1 \leq e \leq K$ such that $C(e) = \text{unit}_{x_i}$. Let then $C_i \in \mathcal{C}$ such that $C_i(K+1) = \ell_i$, $C_i(e) = \text{zero}$ and for all other process $1 \leq j \leq K$, $C_i(j) = C(j)$. By definition of the protocol, $(\ell_i, ?\text{inc}_{x_i}, \ell_{i+1}) \in T$, hence $C_i \xrightarrow{e, (\text{zero}, !!\text{inc}_{x_i}, \text{unit}_{x_i})} C$. Furthermore, it is easy to see that, since $|C_i^{-1}(\text{unit}_{x_i})| = |C^{-1}(\text{unit}_{x_i})| - 1 = v_{i+1}(x_i) - 1 = v_i(x_i)$, and for all other counter x , $|C_i^{-1}(\text{unit}_x)| = |C^{-1}(\text{unit}_x)| = v_{i+1}(x) = v_i(x)$, $C_i \in \mathbb{C}(\ell_i, v_i)$. Hence, by induction hypothesis, $C'_0 \rightarrow^* C_i \rightarrow C$.

Case $\delta_i(x_i) = -1$. Then $v_{i+1}(x_i) = v_i(x_i) - 1 < K$ and there exists a process e such that $C(e) = \text{zero}$. Let $C_i \in \mathcal{C}$ such that $C_i(K+1) = \ell_i$, $C_i(e) = \text{unit}_{x_i}$ and for all other process $1 \leq j \leq K$, $C_i(j) = C(j)$. By definition of the protocol, $(\ell_i, ?\text{dec}_{x_i}, \ell_{i+1}) \in T$, hence $C_i \xrightarrow{e, (\text{unit}_{x_i}, !!\text{dec}_{x_i}, \text{zero})} C$. Furthermore, it is easy to see that, since $|C_i^{-1}(\text{unit}_{x_i})| = |C^{-1}(\text{unit}_{x_i})| + 1 = v_{i+1}(x_i) + 1 = v_i(x_i)$, and for all other counter x , $|C_i^{-1}(\text{unit}_x)| = |C^{-1}(\text{unit}_x)| = v_{i+1}(x) = v_i(x)$, $C_i \in \mathbb{C}(\ell_i, v_i)$. Hence, by induction hypothesis, $C'_0 \rightarrow^* C_i \rightarrow C$.

Then, since $\mathbb{C}(\ell_n, v_n) = \{C'_n\}$ with $C'_n(K+1) = \ell_n = \ell_f$, and $C'_n(j) = \text{zero}$ for all $1 \leq j \leq K$, we have that $C'_0 \rightarrow^* C'_n$. The execution ends in the following way: $C'_n \xrightarrow{1, (\text{zero}, !!\text{end}, \text{z-end})} C'_{n+1} \xrightarrow{2, (\text{zero}, !!\text{end}, \text{z-end})} \dots \xrightarrow{K, (\text{zero}, !!\text{end}, \text{z-end})} C'_{n+K} \xrightarrow{K+1, (s'_f, !!\text{verif}, q_f)} C'_{n+K+1}$. One can observe that for all process e , $C'_{n+K+1}(e) = q_f$ which concludes the proof.

We now prove the other direction, which relies on the following intermediate lemma.

► **Lemma E.2.** *Let $C_0 \rightarrow^* C_f$ such that $C_0 \in \mathcal{I}$ and for all $e \in [1, ||C_f||]$, $C_f(e) = q_f$. Then, the message *verif* is broadcast exactly once.*

Proof. We let $C_0 \rightarrow C_1 \rightarrow \dots \rightarrow C_n = C_f$ be the execution that gathers everyone in q_f . The broadcast of **verif** can be done only with the transition $(\ell'_f, !!\text{verif}, q_f)$.

First, observe that there must exist an index $0 \leq j \leq n$ such that $C_j \xrightarrow{e_1, (\ell'_f, !!\text{verif}, q_f)} C_{j+1}$; otherwise, q_f is unreachable. Let j_1 denote the first such index. If there exists an index $j_2 > j_1$ such that $C_{j_2} \xrightarrow{e_2, (\ell'_f, !!\text{verif}, q_f)} C_{j_2+1}$, then, by construction of the VASS, $e_2 \neq e_1$ (there is now way for e_1 to go back to ℓ'_f), and $C_{j_2+1}(e_1) = \text{err}$. Then, $C_n(e_1) = \text{err}$ which contradicts the definition of the execution. $\blacktriangleleft$

We let $C_0 \rightarrow C_1 \rightarrow \dots \rightarrow C_n = C_f$ be the execution that gathers all processes in q_f . From Lemma E.2, there exists a unique index j such that $C_j \xrightarrow{e_1, (\ell'_f, !!\text{verif}, q_f)} C_{j+1}$. Hence, there exists a unique process e_1 evolving in P_V ; indeed, any process in P_V needs to broadcast **verif** in order to reach q_f , and the only way to broadcast **verif** is to go through P_V . So there exists a unique index $j_1 < j$ such that $C_{j_1} \xrightarrow{e_1, (q_{in}, !!\text{start}, \ell_0)} C_{j_1+1}$. Hence, all other processes receive the message **start** in C_{j_1} ; otherwise they will never reach q_f . As a consequence, for each process $e \neq e_1$, $C_{j_1+1}(e) = \text{zero}$.

We now prove that there exists an index $j_1 < j_2 < j$ such that $C_{j_2}(e_1) = \ell_f$ and for any other process $e \neq e_1$, $C_{j_2}(e) = \text{zero}$. First note that, unless $\ell_0 = \ell_f$, there must be at least one process e such that $C_{j_1+1}(e) = \text{zero}$. Otherwise, no process ever broadcasts a message, and e would not be able to reach ℓ_f and then ℓ'_f .

We claim that j_2 is the first index such that $C_{j_2} \xrightarrow{e_2, (\text{zero}, !!\text{end}, \text{z-end})} C_{j_2+1}$ for some process e_2 . This transition must occur during the execution, otherwise, processes other than e_1 would be unable to reach q_f during the unique broadcast of **verif** in the execution. First, we prove that $C_{j_2}(e_1) = \ell_f$. If it is not the case, it needs to receive some messages in the set $\{\text{inc}_x, \text{dec}_x \mid x \in X\}$ before broadcasting message **!!verif**. Observe from Lemma E.2, there is exactly one broadcast of **verif**, and so the sending of one message in $\{\text{inc}_x, \text{dec}_x \mid x \in X\}$ is received by e_2 which goes to state err' . As a consequence, it never reaches q_f which contradicts definition of C_f . Hence, $C_{j_2}(e_1) = \ell_f$. We prove now that for all other process $e \neq e_1$, $C_{j_2}(e) = \text{zero}$. If it is not the case, since **end** is not broadcast before, there is one process e' such that $C_{j_2}(e') \in \{\text{unit}_x \mid x \in X\}$. In that case, for the same reason as before, it will need to broadcast a message from $\{\text{dec}_x \mid x \in X\}$ while e_2 is in **z-end**. This would lead e_2 to err' and contradicts the definition of C_f .

Hence, $C_{j_2}(e_1) = \ell_f$ and for all other process $e \neq e_1$, $C_{j_2}(e) = \text{zero}$.

We rename the configurations visited during this part of the execution to $C_{j_1+1} = C'_0 \rightarrow C'_1 \rightarrow \dots \rightarrow C'_k = C_{j_2}$, with $k = j_2 - j_1 - 1$. First observe that since $C'_0(e_1) = \ell_0$, and $C'_k(e_1) = \ell_f$, the structure of the VASS implies that $C'_i(e_1) \in \text{Loc}$ for all $0 \leq i \leq k$. Similarly, since, for all $e \neq e_1$, $C'_0(e) = C'_k(e) = \text{zero}$, the structure of the VASS implies that $C'_i(e) \in \{\text{zero}, \text{unit}_x \mid x \in X\}$, for all $e \neq e_1$, and for all $0 \leq i \leq k$. We build now, by induction on $0 \leq i \leq k$, an execution $(\ell_0, v_0) \rightsquigarrow (\ell_1, v_1) \rightsquigarrow \dots \rightsquigarrow (\ell_k, v_k)$ of the VASS such that $\ell_i = C'_i(e_1)$, and $v_i(x) = |C'^{-1}_i(\text{unit}_x)|$ for all $x \in X$.

With $v_0 = \mathbf{0}_X$, the definition of C'_0 allows to conclude immediately for the case $i = 0$. Let now $0 \leq i < k$ and assume the property true. Let $C'_i \xrightarrow{e, t} C'_{i+1}$ the transition taken in the execution of the protocol. Since, by construction of the protocol, $C'_i(e_1) \in Q_W$, we know that $e \neq e_1$, and then $t \in \{(\text{zero}, !!\text{inc}_x, \text{unit}_x), (\text{unit}_x, !!\text{dec}_x, \text{zero}) \mid x \in X\}$. We distinguish two cases:

■ $t = (\text{zero}, !!\text{inc}_x, \text{unit}_x)$ for some $x \in X$. In that case, by definition of the protocol, $(\ell_i, \delta, \ell_{i+1}) \in \Delta$ with $\delta(x) = 1$ and $\delta(x') = 0$ for all other counters x' . Furthermore,

1127 $|C'_{i+1}^{-1}(\text{unit}_x)| = |C'_i{}^{-1}(\text{unit}_x)| + 1 = v_i(x) + 1$ by induction hypothesis, and for all other
 1128 counters x' , $|C'_{i+1}^{-1}(\text{unit}_{x'})| = |C'_i{}^{-1}(\text{unit}_{x'})| = v_i(x')$, by induction hypothesis. Hence,
 1129 $(\ell_i, v_i) \rightsquigarrow (\ell_{i+1}, v_{i+1})$ with $v_{i+1}(x) = |C'_{i+1}^{-1}(\text{unit}_x)|$ for all $x \in X$.
 1130 ■ $t = (\text{unit}_x, \text{!dec}_x, \text{zero})$ for some $x \in X$. In that case, by definition of the protocol,
 1131 $(\ell_i, \delta, \ell_{i+1}) \in \Delta$ with $\delta(x) = -1$ and $\delta(x') = 0$ for all other counters x' . Furthermore,
 1132 $|C'_{i+1}^{-1}(\text{unit}_x)| = |C'_i{}^{-1}(\text{unit}_x)| - 1 = v_i(x) - 1 \geq 0$, by induction hypothesis. For all other
 1133 counters x' , $|C'_{i+1}^{-1}(\text{unit}_{x'})| = |C'_i{}^{-1}(\text{unit}_{x'})| = v_i(x')$, by induction hypothesis. Hence,
 1134 $(\ell_i, v_i) \rightsquigarrow (\ell_{i+1}, v_{i+1})$ where $v_{i+1}(x) = |C'_{i+1}^{-1}(\text{unit}_x)|$ for all $x \in X$.

1135 Hence, $(\ell_0, v_0) \rightsquigarrow^* (\ell_k, v_k)$ with $\ell_k = C'_k(e_1) = C_{j_2}(e_1) = \ell_f$ and $v_k(x) = |C'_k{}^{-1}(\text{unit}_x)| =$
 1136 0 for all counters x , which concludes the proof.

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1138 F Proof of Lemma 4.2

1139 We prove that if there are runs $(s_0, \mathbf{0}) \rightsquigarrow^* (s_f, \mathbf{0})$ and $(s_f, \mathbf{0}) \rightsquigarrow^* (s_0, \mathbf{0})$ in the VASS $\mathcal{V}_P$,
 1140 there is an execution in P that leads all the processes in q_f .

1141 The idea is that from a run of the VASS going from $(s_0, \mathbf{0})$ to $(s_f, \mathbf{0})$, one can build a
 1142 run from $(s_0, \mathbf{0})$ to $(s_f, \mathbf{0})$ that never takes the transition $(s_f, \mathbf{0}, s_0)$. Hence this new run will
 1143 necessarily be of the form: $(s_0, \mathbf{0}) \rightsquigarrow^* (\emptyset, v_{\text{init}}) \rightsquigarrow^* (\emptyset, v_f) \rightsquigarrow (s'_f, v'_f) \rightsquigarrow (s_f, v_f) \rightsquigarrow^*$
 1144 $(s_f, \mathbf{0})$ with no visit of $(s_f, \mathbf{0}, s_0)$. Here, $v_{\text{init}}(x) = 0$ for all counters $x \neq x_{q_{\text{in}}}$ and $v_f(x) = 0$
 1145 for all counters $x \neq x_{q_f}$. An easy adaptation of the proof of Lemma 3.9 allows then to build
 1146 an execution of the protocol from $C_0 \in \mathcal{I}$ to C_f such that for all $e \in [1, ||C_f||]$, $C_f(e) = q_f$.

1147 We now focus on transforming the run of the $\mathcal{V}_P$. From the VASS construction, we can
 1148 decompose the run into: $(s_0, v_0) \rightsquigarrow^* (s_f, v_1) \rightsquigarrow (s_0, v_1) \rightsquigarrow^* (s_f, v_2) \dots (s_0, v_{n-1}) \rightsquigarrow^*$
 1149 (s_f, v_n) with $v_0 = v_n = \mathbf{0}$. Moreover, for all $0 \leq i < n$ there exist vectors v'_i , v''_i and v'''_i such
 1150 that: $(s_0, v_i) \rightsquigarrow^* (s_0, v'_i) \rightsquigarrow (\emptyset, v'_i) \rightsquigarrow^* (\emptyset, v''_i) \rightsquigarrow (s'_f, v'''_i) \rightsquigarrow (s_f, v''_i) \rightsquigarrow^* (s_f, v_{i+1})$,
 1151 with (i) $v'_i(x_{q_{\text{in}}}) = v_i(x_{q_{\text{in}}}) + k$ for some $k \in \mathbb{N}$ and for all other counters x , $v'_i(x) = v_i(x)$, and
 1152 (ii) $v''_i(x_{q_f}) = v_{i+1}(x_{q_f}) + j$ for some $j \in \mathbb{N}$ and for all other counters x , $v''_i(x) = v_{i+1}(x)$.

1153 The key observation is to note that for all $s, s' \in \text{Loc}$, for all counter valuations v_1, Δ_1, v_2
 1154 such that $\mathbf{0} \preceq \Delta_1$, with $\preceq$ the component-wise order, if $(s, v_1) \rightsquigarrow^* (s', v_2)$ then $(s, v_1 +$
 1155 $\Delta_1) \rightsquigarrow^* (s', v_2 + \Delta_1)$. By reordering, we construct a run where all increments of $x_{q_{\text{in}}}$ occur
 1156 at the beginning and all decrements of x_{q_f} occur at the end.

1157 For all $0 \leq i < n$, let Δ_i be defined by:

- 1158 ■ $\Delta_i(x_{q_{\text{in}}}) = \sum_{i < j < n} v'_j(x_{q_{\text{in}}}) - v_j(x_{q_{\text{in}}})$
- 1159 ■ $\Delta_i(x_{q_f}) = \sum_{0 \leq j < i} v''_j(x_{q_f}) - v_{j+1}(x_{q_f})$
- 1160 ■ $\Delta_i(x) = 0$ for all other $x \in X$.

1161 Observe first that $\mathbf{0} \preceq \Delta_i$, and that, for all $0 \leq i < n$, $v''_i + \Delta_i = v'_{i+1} + \Delta_{i+1}$. Then, from
 1162 the different parts of the run $(\emptyset, v'_i) \rightsquigarrow^* (\emptyset, v''_i)$, we deduce that $(\emptyset, v'_i + \Delta_i) \rightsquigarrow^* (\emptyset, v''_i + \Delta_i)$,
 1163 and we can hence build an execution $(s_0, \mathbf{0}) \rightsquigarrow^* (s_0, v'_0 + \Delta_0) \rightsquigarrow (\emptyset, v'_0 + \Delta_0) \rightsquigarrow^* (\emptyset, v'_1 +$
 1164 $\Delta_1) \dots \rightsquigarrow^* (\emptyset, v''_{n-1} + \Delta_{n-1})$.

1165 Since in the original run $(\emptyset, v'_{n-1}) \rightsquigarrow^* (\emptyset, v''_{n-1}) \rightsquigarrow (s'_f, v'''_{n-1}) \rightsquigarrow (s_f, v''_{n-1}) \rightsquigarrow^*$
 1166 (s_f, v_n) , with $v_n = \mathbf{0}$, we deduce that $v''_{n-1}(x_{q_{\text{in}}}) = \mathbf{0}$, $v''_{n-1}(x_{q_f}) > 0$, and $v''_{n-1}(x) = 0$ for
 1167 all other $x \in X$. Moreover, $\Delta_{n-1}(x) = 0$ for all $x \neq x_{q_f}$, hence can continue the execution
 1168 $(\emptyset, v''_{n-1} + \Delta_{n-1}) \rightsquigarrow (s'_f, v'''_{n-1} + \Delta_{n-1}) \rightsquigarrow (s_f, v''_{n-1} + \Delta_{n-1}) \rightsquigarrow^* (s_f, \mathbf{0})$.

1169 This run avoids the transition $(s_f, \mathbf{0}, s_0)$.

1170 Observe that if $q_f \in Q_W$, then this transformation is not possible, as run segments between
 1171 s_0 and s_f would be of the form $(s_0, v) \rightsquigarrow^* (\emptyset, v_1) \rightsquigarrow^* (\{S_f\}, v_2) \rightsquigarrow (s'_f, v_3) \rightsquigarrow (s_f, v_3)$.

As a result, we can not shorten a run by gluing a configuration with location $\{S_f\}$ and one with location $\emptyset$.

G Proofs of Section 5

We prove here the soundness and correctness of the reduction presented in Section 5. Denote $\mathcal{L}(\mathcal{A})$ the language of automaton $\mathcal{A}$, i.e. $\mathcal{L}(\mathcal{A}) = \{w \mid \Delta^*(q^0, w) = q^f\}$ where q^0 is its initial state and q^f its final state.

► **Lemma G.1.** *If there exists a word $w \in \bigcap_{1 \leq i \leq n} \mathcal{L}(\mathcal{A}_i)$, then there exists an execution $\rho \in \Lambda_\omega$ and $e \in [1, \#proc(\rho)]$ such that for all $i \in \mathbb{N}$, there exists $j > i$ such that $\rho_j \xrightarrow{e, (q_{n+2}, !!\odot, q_1)} \rho_{j+1}$.*

Proof. We denote $w = a_1 \dots a_k$. Let C_0 be the initial configuration with $n + 2$ processes. We form the following prefix of execution:

$$C_0 \xrightarrow{1, (q_{in}, !!1, q_1^0)} C_1 \xrightarrow{2, (q_{in}, !!2, q_2^0)} \dots \xrightarrow{n, (q_{in}, !!n, q_n^0)} C_n \xrightarrow{n+1, (q_{in}, !!\#, q_1)} C_{n+1}$$

C_{n+1} is such that: $C_{n+1}(i) = q_i^0$ for all $i \in [1, n]$, and $C_{n+1}(n+1) = q_1$ and $C_{n+1}(n+2) = q_{in}$. Next, consider the following sequence of configurations:

$$\begin{aligned} C_{n+1} &\xrightarrow{n+2, (q_{in}, !!a_1, q_{in})} C_{n+2} \xrightarrow{n+2, (q_{in}, !!a_2, q_{in})} \dots \xrightarrow{n+2, (q_{in}, !!a_k, q_{in})} C_{n+k+1} \\ &\xrightarrow{n+2, (q_{in}, !!\$, q_{in})} C_{n+k+2} \xrightarrow{1, (q_1^{\$}, !!end_1, q_1^e)} \dots \xrightarrow{n, (q_n^{\$}, !!end_n, q_n^e)} C_{2n+k+2} \\ &\xrightarrow{n+1, (q_{n+2}, !!\odot, q_1)} C_{2n+k+3} \end{aligned}$$

As $a_1 \dots a_k = w \in \bigcap_{1 \leq i \leq n} \mathcal{L}(\mathcal{A}_i)$, it holds that: $C_{n+k+1}(i) = q_i^f$ for all $i \in [1, n]$. Hence, $C_{n+k+2}(i) = q_i^{\$}$ for all $i \in [1, n]$. Furthermore, $C_{n+k+2}(n+1) = q_2$. We conclude that $C_{2n+k+3} = C_{n+1}$. Hence, we can form an infinite execution $\rho = C_0 C_1 \dots C_{n+1} \dots C_{2n+k+3} \dots C_{3n+k+5} \dots$ such that for all $i \in \mathbb{N}$, $C_{n+1+i(n+k+2)} = C_{n+1}$ and $C_{n+i(n+k+2)} \xrightarrow{n+1, (q_{n+2}, !!\odot, q_1)} C_{n+1+i(n+k+2)}$, which concludes the proof. ◀

► **Lemma G.2.** *If there exists an execution $\rho \in \Lambda_\omega$ and $e \in [1, \#proc(\rho)]$ such that for all $i \in \mathbb{N}$, there exists $j > i$ such that $\rho_j \xrightarrow{e, (q_{n+2}, !!\odot, q_1)} \rho_{j+1}$, then there exists $w \in \bigcap_{1 \leq i \leq n} \mathcal{L}(\mathcal{A}_i)$.*

Proof. Denote ρ such an execution. We start by proving the following lemma.

► **Lemma G.3.** *There exists $i_0 \in \mathbb{N}$, from which no new processes reach state $\odot$, in other words, for all process e , if $\rho_{i_0}(e) \neq \odot$ then $\rho_i(e) \neq \odot$ for all $i > i_0$.*

Proof. We reason by contradiction. Otherwise, for all $i_0 \in \mathbb{N}$, either all processes are on $\odot$, or there exists a process not in $\odot$ who reach it later. As $\odot$ is a sink state and there is a finite number of processes, eventually all processes are on $\odot$, leading to a deadlock configuration, which contradicts the fact that $\rho \in \Lambda_\omega$. ◀

We now denote e_0 a process taking infinitely often transition $(q_{n+2}, !!\odot, q_1)$. We easily see that for all $i \in \mathbb{N}$, $\rho_i(e_0) \neq \odot$, as otherwise, $\rho_j(e_0) \neq \odot$ for all $j > i$. Consider now $i_1, i_2, i_3 \in \mathbb{N}$ three indexes such that $i_0 < i_1 < i_2 < i_3$. and $\rho_{i_j} \xrightarrow{e_0, (q_{n+2}, !!\odot, q_1)} \rho_{i_{j+1}}$ for $j \in \{1, 2, 3\}$. By construction of the protocol, there exist $e_1, \dots, e_n$ which respectively broadcast messages

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1208 $\text{end}_1, \dots, \text{end}_n$ which are received by e_0 between i_1 and i_2 . Observe that from the protocol's
 1209 construction, once process e_0 is on state q_2 , the only sequence of broadcast messages that
 1210 does not put e_0 (before it reaches q_{n+2}) in $\ominus$ is $\text{end}_1 \cdot \text{end}_2 \cdots \text{end}_n$. Meaning that there
 1211 exists $i_1 < i_4 < i_2$ such that $C_{i_4}(e_0) = q_2$ and:

$$1212 \quad C_{i_4} \xrightarrow{e_1, (q_1^{\$}, !!\text{end}_1, q_1^e)} \xrightarrow{e_2, (q_2^{\$}, !!\text{end}_2, q_2^e)} \cdots \xrightarrow{e_n, (q_n^{\$}, !!\text{end}_1, q_n^e)} C_{i_4+n}$$

1213 as any other behaviour would put e_0 in $\ominus$. Observe that for all $i \in [1, n]$, $C_{i_4+n}(e_i) = q_i^e$.
 1214 Note that $i_4 + n = i_2$ as if $C_{i_4+n} \xrightarrow{(q, !!m, q')} C_{i_4+n+1}$ with $m \neq \ominus$, then $C_{i_4+n}(e_n) = \ominus$.
 1215 Hence:

$$1216 \quad C_{i_2}(e_i) = q_i^0 \text{ for all } i \in [1, n]$$

$$1217 \quad C_{i_2}(e_0) = q_1$$

1218 Denote i_5 the next index from which $\$$ is broadcast (it exists as, in particular, e_0 needs
 1219 to take the cycle with $(q_2, ?\text{end}_1, q_3)$).

1220 As $i_3 > i_0$, no process e_i reaches $\ominus$, hence $\rho_{i_5}(e_i) = q_i^f$ as otherwise, $\rho_{i_5+1}(e_i) = \ominus$ for
 1221 all $i \in [1, n]$.

1222 Observe that between $i_3 + 1$ and i_5 , the only broadcasts are of the form $(q_{in}, !!a, q_{in})$ with
 1223 $a \in \Sigma$ as any other broadcast would put e_0 in $\ominus$.

1224 Hence, the sequence of letters $a \in \Sigma$ broadcast between $i_3 + 1$ and i_5 is received by all
 1225 processes $e_1, \dots, e_n$ (as automata are complete), and thanks to the protocol's construction,
 1226 it forms a word accepted by all automata. ◀